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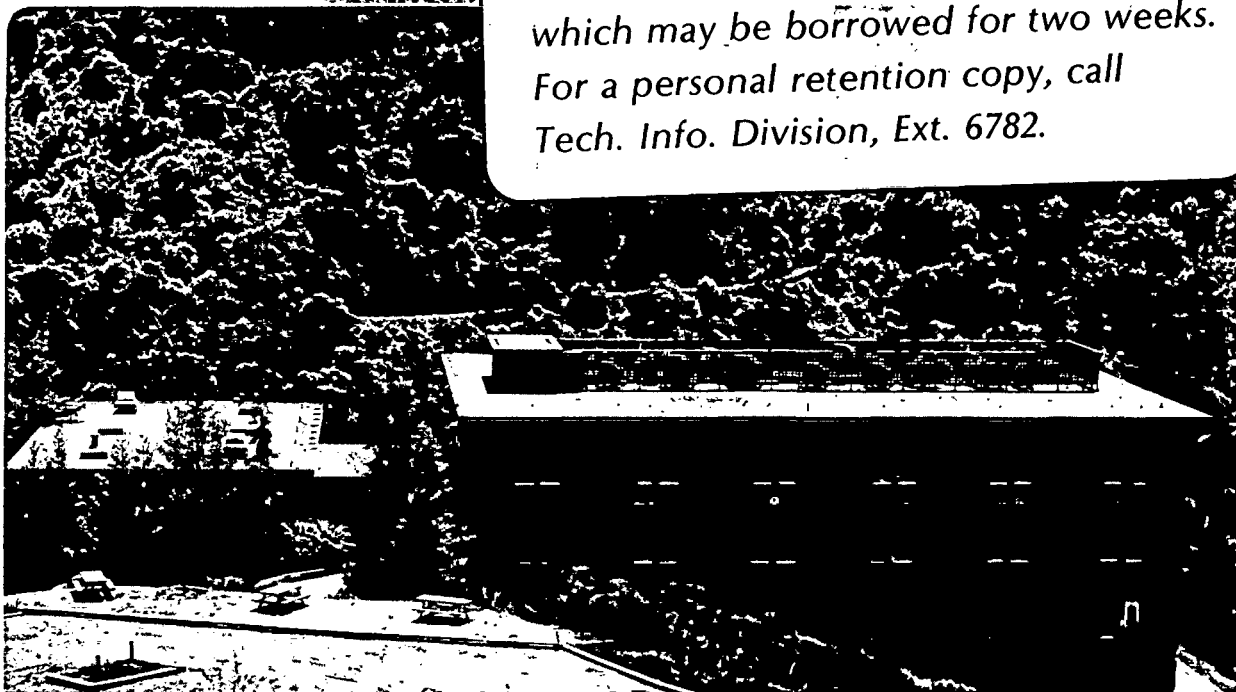
THE EFFECT OF RECOILED O ON DAMAGE REGROWTH  
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IMPLANTED Si

D.K. Sadana, N.R. Wu, J. Washburn, M. Current,  
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THE EFFECT OF RECOILED O ON DAMAGE REGROWTH AND ELECTRICAL  
PROPERTIES OF THROUGH-OXIDE IMPLANTED Si

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ABSTRACT

High dose ( $4-7.5 \times 10^{15} \text{ cm}^{-2}$ ) As implantations into p-type (100) Si have been carried out through a screen-oxide of thicknesses  $\leq 775\text{\AA}$  and without screen oxide. The effect of recoiled O on damage annealing and electrical properties of the implanted layers has been investigated using a combination of the following techniques: TEM, RBS/MeV  $\text{He}^+$  channeling, SIMS and Hall measurements in conjunction with chemical stripping and sheet resistivity measurements. The TEM results show that there is a dramatically different annealing behavior of the implantation damage for the through oxide implants (Case I) as compared to implants into bare silicon (Case II). Comparison of the structural defect profiles with O distributions obtained by SIMS demonstrated that retardation in the secondary damage growth in Case I can be directly related with the presence of O. Weak-beam TEM showed that a high density of fine defect clusters ( $\leq 50\text{\AA}$ ) were present both in Case I and Case II. The electrical profiles showed only 30 percent of the total As to be electrically active. The structural and electrical results have been explained by a model that entails As-O, Si-O and As-As complex formation and their interaction with the dislocations.

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## INTRODUCTION

Implants through thin dielectric layers (oxides and nitrides) are common features of IC device processing (1). The principal advantages of this procedure over implanting directly into bare Si include:

(a) process simplicity and implant depth modulation (2,3), (b) protection of device areas from contamination from sputtered material during implanting (4) and (c) improved control on device parameters such as leakage currents (1,5) and gettering efficiency (6).

For implants where the peak of the profile is at a depth comparable to the dielectric layer thickness, copious quantities of atoms from the screen layer are "recoil implanted" into the substrate region (7,8). Monte Carlo (9) and analytical (10-12) methods have been applied to the calculation of the number of oxygen atoms which are recoil-implanted into Si for implants through thin oxide layers. Application of Boltzmann transport equation methods (12) has predicted recoil yield and oxygen depth profiles which have been experimentally verified in this study and by others (13).

For a high dose ( $10^{15}$  ions  $\text{cm}^{-2}$ ) through a screen oxide, the concentration of recoil implanted oxygen at the oxide interface can be greater than  $10^{21}$  oxygen atoms  $\text{cm}^{-3}$ . The presence of these high levels of oxygen ( $\sim 10^3$  times higher than oxygen solubility limits at  $-1200^\circ\text{C}$ ), has a profound effect on the nature and density of lattice defects present after high temperature annealing (14-15). A strong pinning effect on dislocation loops from recoil implanted oxygen (17) was observed in this study for anneals in  $\text{N}_2$  and oxidizing ambients.

Comparative studies of the residual defects and electrical properties of annealed implants through screen oxides have also indicated significant differences between Si of (100) and (111) orientations (18).

#### EXPERIMENTAL

The experimental program was designed to be comparable to the general processing conditions of an all-implanted, MOS device fabrication technology. The specific implants discussed in this paper were processed with simplified "standard" conditions. The wafer material was p-Si(100), 7-33 ohm-cm Boron doped. All wafers were oxidized before implantation to the full oxide thickness. Thinner screen oxides and "bare Si" conditions were obtained by controlled etching in a buffered oxide etch. In this way, all wafers were subjected to similar thermal processing (initial oxidation at 950°C and post-implant anneals).

The implants were done with As ion beams of 4 to 12 mA for doses of 4 to  $7 \times 10^{15}$  As<sup>+</sup> cm<sup>-2</sup> at 100 and 120 keV. The wafers were either "bare Si" or with oxides of 300, 475, or 775 Å. The principal anneal cycle used in this study was 30 minutes in N<sub>2</sub> at 1000°C. Additional tests were done for annealing temperatures between 700 and 1050°C as well as annealing at 1000°C in oxidizing ambients with HCl of 0, 3 or 10 percent.

The analytical techniques employed include extensive use of TEM (in plan and cross-section (XTEM)), RBS (with 1.5 MeV He<sup>+</sup> beams in channeled or random orientation) and SIMS (Ar<sup>+</sup> and Cs<sup>+</sup> beams with a Cameca 3F system). Electrical characterization was done with surface

sheet resistance measurements with a four-point probe (19) and Hall mobility and carrier concentration measurements done in conjunction with anodic etching depth profiling.

## RESULTS

### A. Pinning of Structural Defects by O

The TEM cross-section (XTEM) micrographs from the samples that were implanted through 30Å ("bare-Si"), 475Å and 775Å screen oxides, all showed continuous amorphous layers extending from the SiO<sub>2</sub>/Si interfaces with progressively decreasing widths being 1050Å, 1000Å, and 680Å, respectively. This was expected from the reduced range of As atoms in the Si substrate with thicker screen oxides. The thickness of 775Å correspond to the projected range of As in SiO<sub>2</sub> at 120 keV and therefore only about 50 percent of the As atoms penetrated the Si in this case. The As distributions obtained by both RBS and SIMS from these samples showed approximately gaussian distributions. The XTEM micrographs and atomic distributions from some of the samples have been reported earlier (17).

Figures 1a, b, and c, show weak-beam TEM plan view micrographs from the samples described above after annealing them at 1000°C for 1/2 hour in N<sub>2</sub> (with ramp up from 500°C and ramp down to 900°C). The corresponding cross-section micrographs (bright-field TEM) are shown in Figs. 2a, 3a and 4a, respectively. It is clear from these results that the amorphous layers have recrystallized by solid phase epitaxy on the undamaged Si substrate. In the bare Si case, the residual lattice defects are in the form of dislocation half-loops

which intersect the surface of the bare Si and extend several thousand Angstroms into the wafer. This type of lattice defect structure has been discussed many times in the literature (15,17). In the case of thorough-oxide implantation, the defects were localized into discrete layers (Figs. 3a and 4a). The XTEM results from the 475Å and 775Å screen-oxide samples are similar in that well defined dense layers of dislocation loops form at depths which are slightly deeper than the amorphous/crystalline interface present immediately after high dose implantation. In addition, small defect clusters were present in the near surface region (0-250Å) of these samples. In the 475Å case there was an extra layer of small dislocation loops (350Å across, Fig. 2a) between the surface and the deeper dense layer of dislocations.

The oxygen distributions (Figs. 2b, 3b, and 4b) before and after the annealing were obtained by SIMS and the results are directly compared with structural defects distributions from the XTEM micrographs (Figs. 2, 3 and 4). It is clear from these results that the positions of the layers densely populated with dislocation loops matches precisely with the peaks in the O profiles after annealing. Similar association of O with defects has been speculated in results reported recently although direct correlation with discrete defect layers was lacking (1,5,7).

#### B. As, Carrier Concentration Profiles

Figure 5 shows the As and carrier concentration profiles from the annealed samples in which As was implanted through 30 Å (bare), 475 Å and 775Å screen oxides. For comparison, As distributions in the un-



annealed samples have also been included. No local peaks (20) in the atomic As distributions were observed for the annealed material. The broad plateau with concentrations  $2-3 \times 10^{20} \text{ cm}^{-3}$  over the depth ranges  $0-0.3 \mu\text{m}$  followed by a sharp decline was in agreement with earlier results (21). The As spectra obtained in a channeling direction by RBS indicated that a large fraction (~50 percent) of the atoms were located in non-substitutional positions. The non-substitutional As distribution did show a peak at  $\sim 1000 \text{ \AA}$  for the  $775 \text{ \AA}$  screen oxide case which correlates well with the position of the layer of dislocation loops in Fig. 4a (23). The metallurgical junction depths varied with oxide thickness from  $0.25$  to  $0.4 \mu\text{m}$ . The results of structural studies, atomic profiles and junction depths as a function of screen-oxide thickness are summarized in Fig. 6.

Comparison of As and carrier concentration profiles show that only 30-40 percent of the As is electrically active (22). The average mobilities in the depth range  $0-2000 \text{ \AA}$  were  $\sim 600-800 \text{ cm}^2/\text{volt-sec}$  and were in general agreement with high concentration mobilities in Si.

### C. Effect of Annealing Ambient on Defects Formation

A summary of the results of the annealing behavior of the amorphous layers in bare and through oxide ( $\sim 300 \text{ \AA}$ ) implanted (100) Si in various ambients, such as,  $\text{N}_2$ ,  $\text{O}_2$  and  $\text{N}_2+\text{O}_2+\text{HCl}$  and their combinations is given in Table 1. The TEM micrographs in this study were taken under weak-beam diffraction conditions using a 220 type g vector (micrographs not included in the text). The implantation conditions discussed in this section were  $100 \text{ keV}$  and a dose of  $5 \times 10^{15} \text{ cm}^{-2}$ .

The As implants were done with an AIT-IIIX implanter using a beam current of 12 mA. The following points were noted while comparing the results of bare and through-oxide cases listed in Table 1. (1) More restricted growth of defects (dislocation loops or networks) occurred in the case of thorough-oxide implants as compared to the bare implants, irrespective of the ambient used, (2) the annealing out of the defects in both cases accelerated in the presence of  $O_2$  or  $O_2+HCl$  ambient, (3) the addition of HCl up to 10 percent in  $O_2$  did not significantly enhance the annealing out of the defects, as compared to that obtained by  $O_2$  only, and (4) all of the samples (bare and through-oxide) contained a high density of fine defect clusters in the background under all annealing conditions. These defect clusters were not easily detectable under bright-field diffraction conditions and therefore have not been considered in the bare implanted Si in the previously published literature (1).

#### DISCUSSION

It is clear from these results that several types of secondary defects originate during annealing: fine defect clusters, straight  $a/2$   $\langle 110 \rangle$  dislocation lines running very close to the surface, small dislocation loops, large half loops of dislocations intersecting the surface and dislocation networks. In most of the samples, two or three of these types of defects were present, in addition, the fine defect clusters ( $<50 \text{ \AA}$ ) were present in all cases. Figures 1b and 1c also show that the shapes of the dislocation loops are quite irregular, indicating that dislocations are interacting with fine scale defect clusters.

Comparison of the recoiled-oxygen curves from the unannealed and annealed samples indicate that some of the O is lost in the annealing process. Since it has been shown that the redistribution of O can occur at temperatures as low as 450°C (13), it is expected that O would diffuse interstitially both toward the surface and deeper into the material in the early stages of high temperature annealing. The local peak of O should therefore form by gettering of O to the layer of dislocation loops that begin to nucleate at 500-550°C. The O atoms are expected to react chemically with Si during the annealing to form stable  $\text{SiO}_x$  complexes which appear as fine defect clusters in Fig. 1 and restrict the growth of the dislocation loops. The gettering of O to the loops is confirmed by the results of Figs. 3 and 4 where the two different thicknesses of the screen-oxide, the dense layers of the dislocation loops nucleated at different depths from the  $\text{SiO}_2/\text{Si}$  interface and the positions of the O peaks also shifted accordingly.

During the annealing of the implanted Si in an  $\text{O}_2$  atmosphere, the following dynamic events are expected to be in progress: 1) The oxidation of Si at the surface which releases Si interstitials into the wafer (24). 2) As at the the  $\text{SiO}_2/\text{Si}$  interface is being pushed into the material because of its low solubility in  $\text{SiO}_2$  (the so called snow-ploughing effect) (25). 3) The implantation induced amorphous layer regions regrow and the large number of As and Si interstitials within the implanted region precipitate to form dislocation lines and loops. 4) In the case of the through-oxide implant, the recoiled

oxygen is expected to interact with the Si interstitials and nucleate Si-O complexes (19). 5) Additionally, because of the high dose of As, As precipitation may occur (26). The presence of the fine defect clusters in all the samples studied suggests that the clusters are not just Si-O complexes, but also involve As. A number of different defect clusters, consisting of As-As or As-Si complexes in the bare implanted Si and As-O, As-Si-O in the through oxide implanted are possible. The involvement of O in clustering is supported by the results of Figs. 2, 3, and 4, while As clustering is indicated by the electrical and MeV He<sup>+</sup> channeling measurements. However, the precise composition of these defect clusters is unknown.

For the implant and anneal conditions used in this study, the major defect structures are located at depths less than half of the junction depth, particularly for the screen oxide implants. The contributions of defect penetration of the active junction along the vertical direction to junction leakage in MOS capacitor structures were found to be minimal (5,17). The effect of the fine defect clusters near the surface on leakage current is not yet clear.

#### CONCLUSIONS

The major findings of this study are:

1. Differences in nature and location of residual defects were seen for As implants into bare Si compared to through screen oxides of 500 to 800 Å. Pinning of dislocation loops by oxygen recoil-implant occurred in the through-oxide implanted case.

2. Local segregation of oxygen was observed by SIMS at depths corresponding to residual defect layers (observed by XTEM and channeld RBS).
3. High densities of small ( $<50\text{\AA}$ ) defect clusters were observed by weak-beam TEM. These may be associated with As and oxygen complexes with silicon.
4. Systematic variation in junction depths and location of defect layers with oxide layer thickness at 120 kev are consistent with the depth of penetration of As and oxygen into the Si.

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Table 1.

Ambients (Annealing at 1000°C)	As → (100) Si $t_{ox} = 30\text{Å}$			As → SiO <sub>2</sub> /(100), Si $t_{ox} = 300\text{Å}$		
	$t_{ox}$	Structural Defects Data	R Ω/□	$t_{ox}$ after annealing (Å)	Structural Defects Data	R Ω/□
N <sub>2</sub> (1/2 hr)	34	low density of big loops (2000Å), some dislocation lines, fine defect clusters	29.82	218	entangled dislocations, fine defect clusters and small dislocation loops (~500Å)	32.71
N <sub>2</sub> (1/2 hr) + N <sub>2</sub> (1/2 hr)	40	Same as No. 1 but with fewer loops and dislocation lines, fine defect clusters	25.37	259	two sets of dislocation 1st set 500-1000Å, 2nd set <200Å; fine defect clusters	27.84
N <sub>2</sub> (1/2 hr) + O <sub>2</sub> (1/2 hr)	579	a/2 <110> straight dislocations, fine defects	27.09	698	small dislocation loops (~500Å), high density of fine defect clusters	33.27
O <sub>2</sub> (1/2 hr)	643	same as No. 3	30.41	692	a dislocation network, fine defect clusters	34.71
O <sub>2</sub> + 10 HCl (1/2 hr)	1791	same as No. 3	34.82	1005	same as No. 3 but fewer fine defect clusters	34.71
N <sub>2</sub> + O <sub>2</sub> + 10 HCl (1/2 hr)	1375	fine defect clusters no dislocations	30.33	993	same as No. 3 but fewer fine defect clusters	29.97

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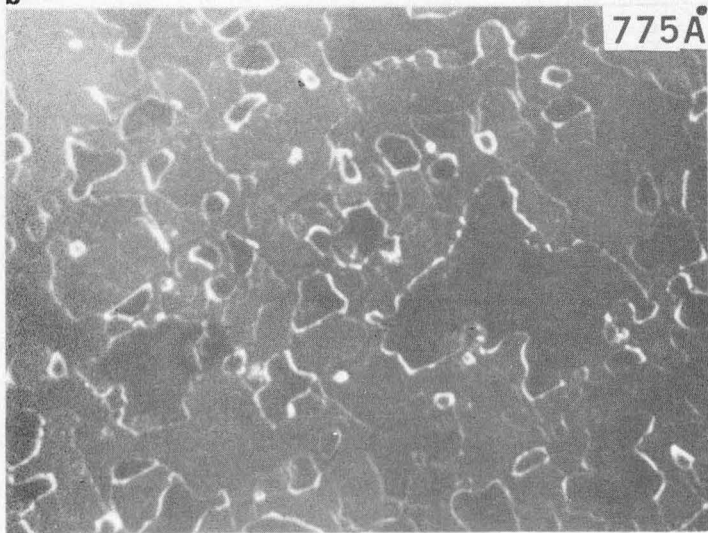
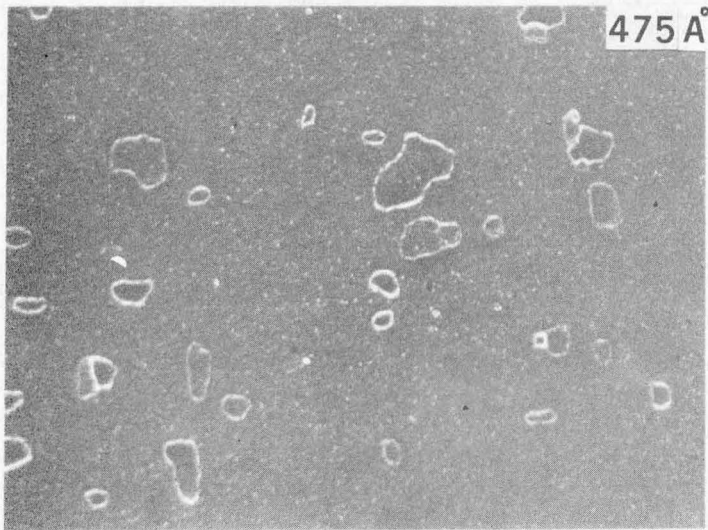
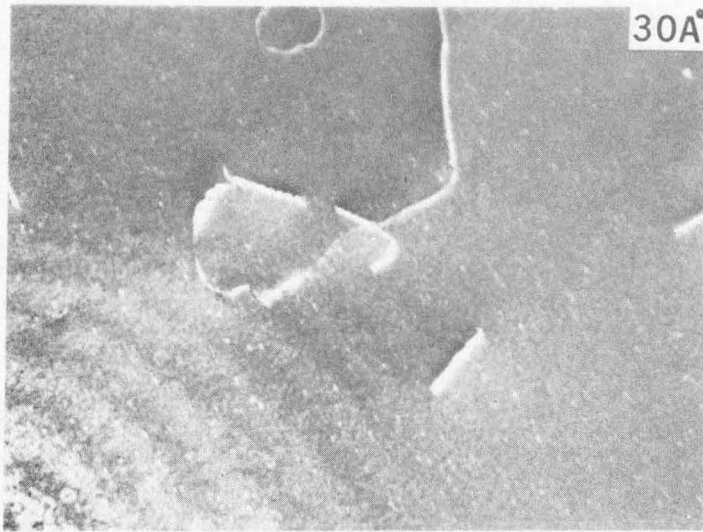
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## FIGURES

- Figure 1. Weak-beam plan-view TEM micrographs in a (220) type reflection showing fine defect clusters and pinned dislocation loops (a)  $t_{\text{ox}} = 30\text{\AA}$ , (b)  $t_{\text{ox}} = 475\text{\AA}$  and (c)  $t_{\text{ox}} = 775\text{\AA}$ .
- Figure 2. Comparisons of (a) the structural-defects profile (XTEM) with (b) O profile (SIMS). No local O peak occur for  $t_{\text{ox}} = 30\text{\AA}$  (bare Si).
- Figure 3. Comparison of (a) the structural-defect profile (XTEM) with (b) O profile (SIMS). Local segregation of O occurs at the densely populated layer of dislocation loops ( $t_{\text{ox}} = 475\text{\AA}$ ).
- Figure 4. Comparison of (a) the structural-defects profile (XTEM) with (b) O profile (SIMS). Local segregation of O occurs at the layer of dislocation loops ( $t_{\text{ox}} = 775\text{\AA}$ ).
- Figure 5. Comparison between As and carrier concentration profiles (a)  $t_{\text{ox}} = 30\text{\AA}$ , (b)  $t_{\text{ox}} = 475\text{\AA}$ , and (c)  $t_{\text{ox}} = 775\text{\AA}$ .
- Figure 6. Defects location and junction depth as a function of screen-oxide thickness. Line labeled R Fair (1972) (ref. 27) shows the junction depths of As capsuled diffusion.



0.3 μm

Fig. 1

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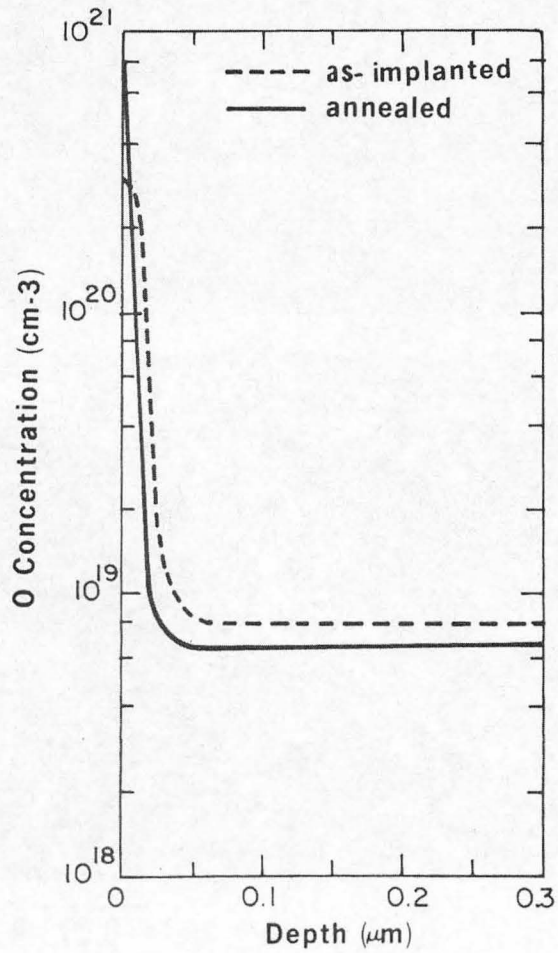
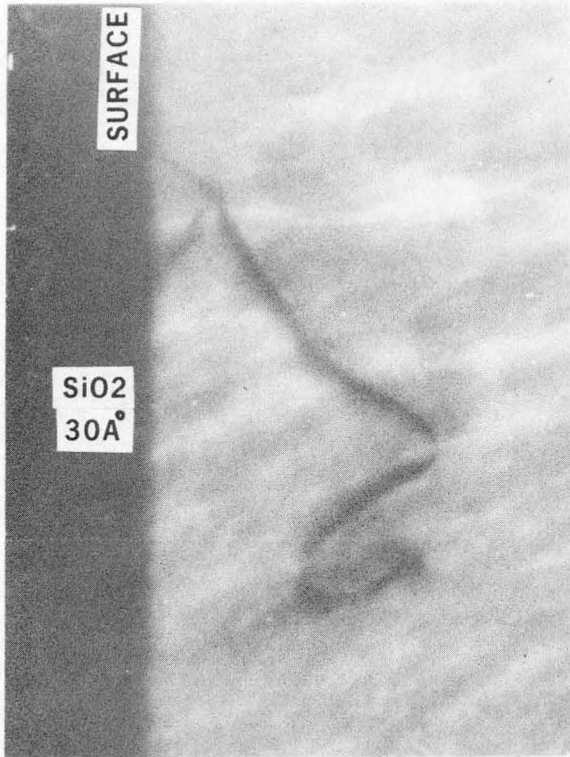


Fig. 2

XBB 828-7443

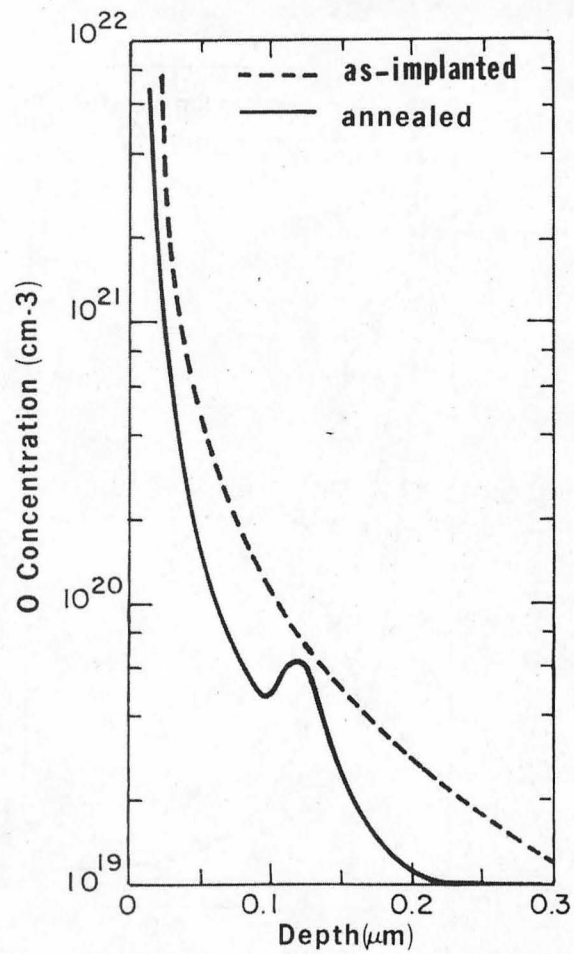
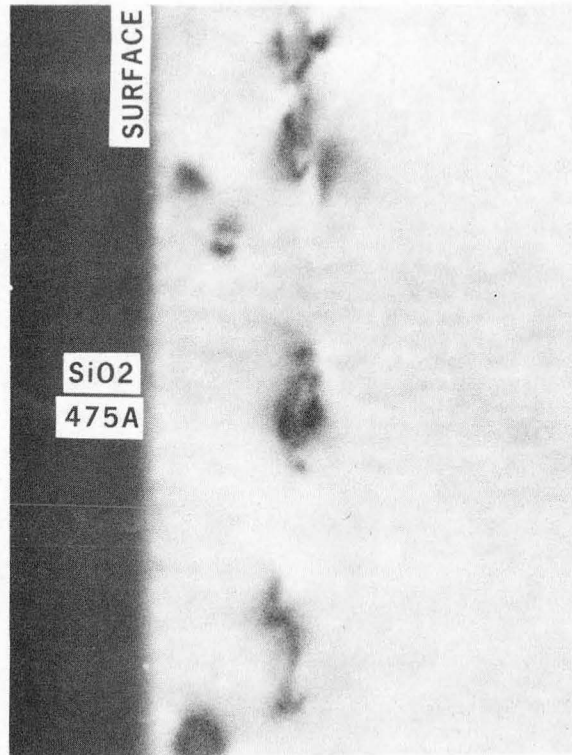


Fig. 3

XBB 828-7445

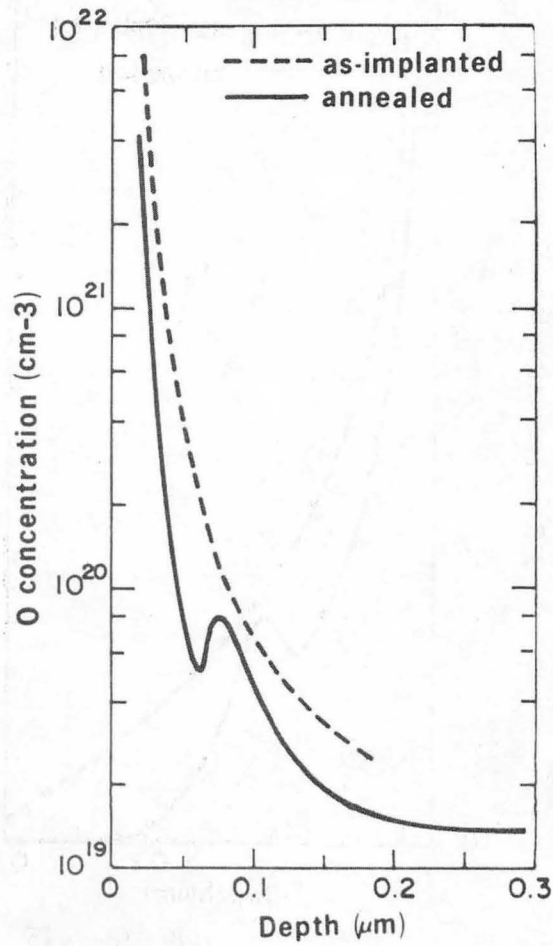
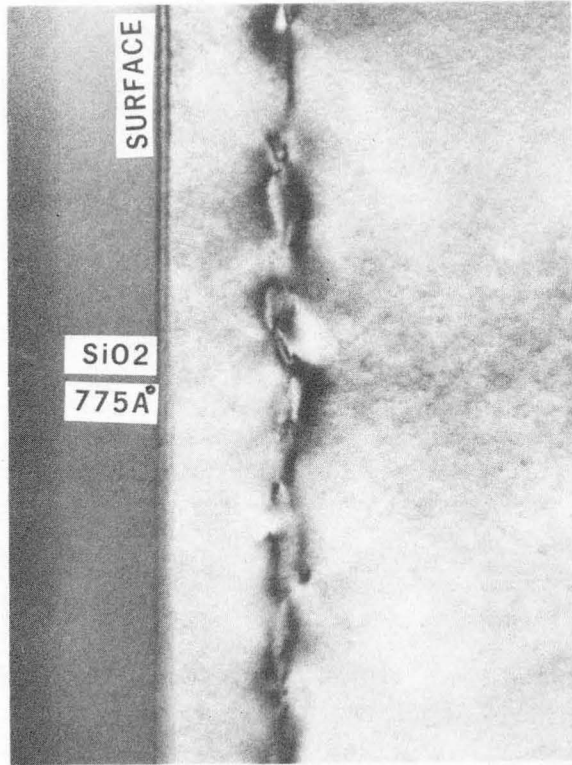


Fig. 4

XBB 828-7444

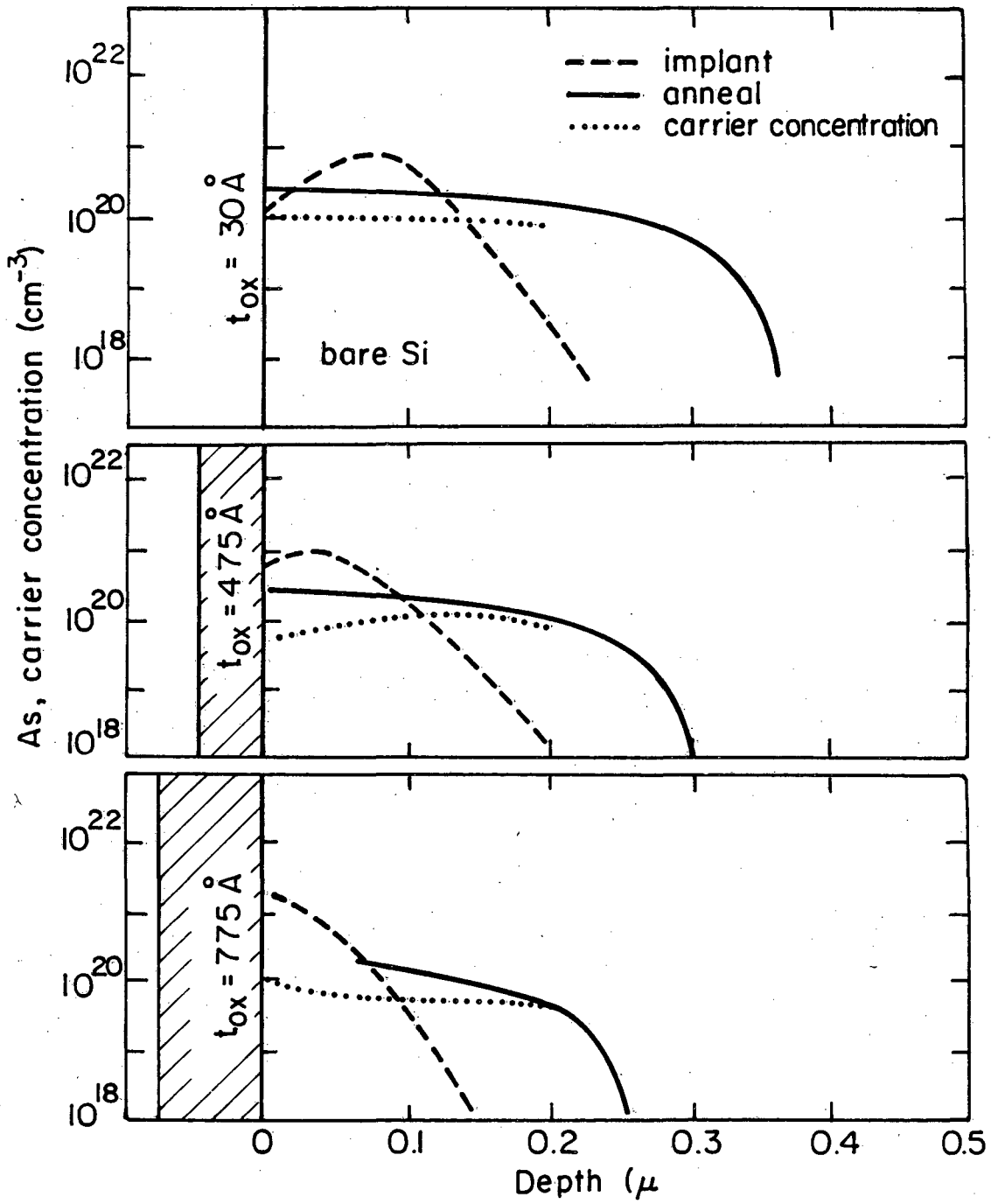


Fig. 5

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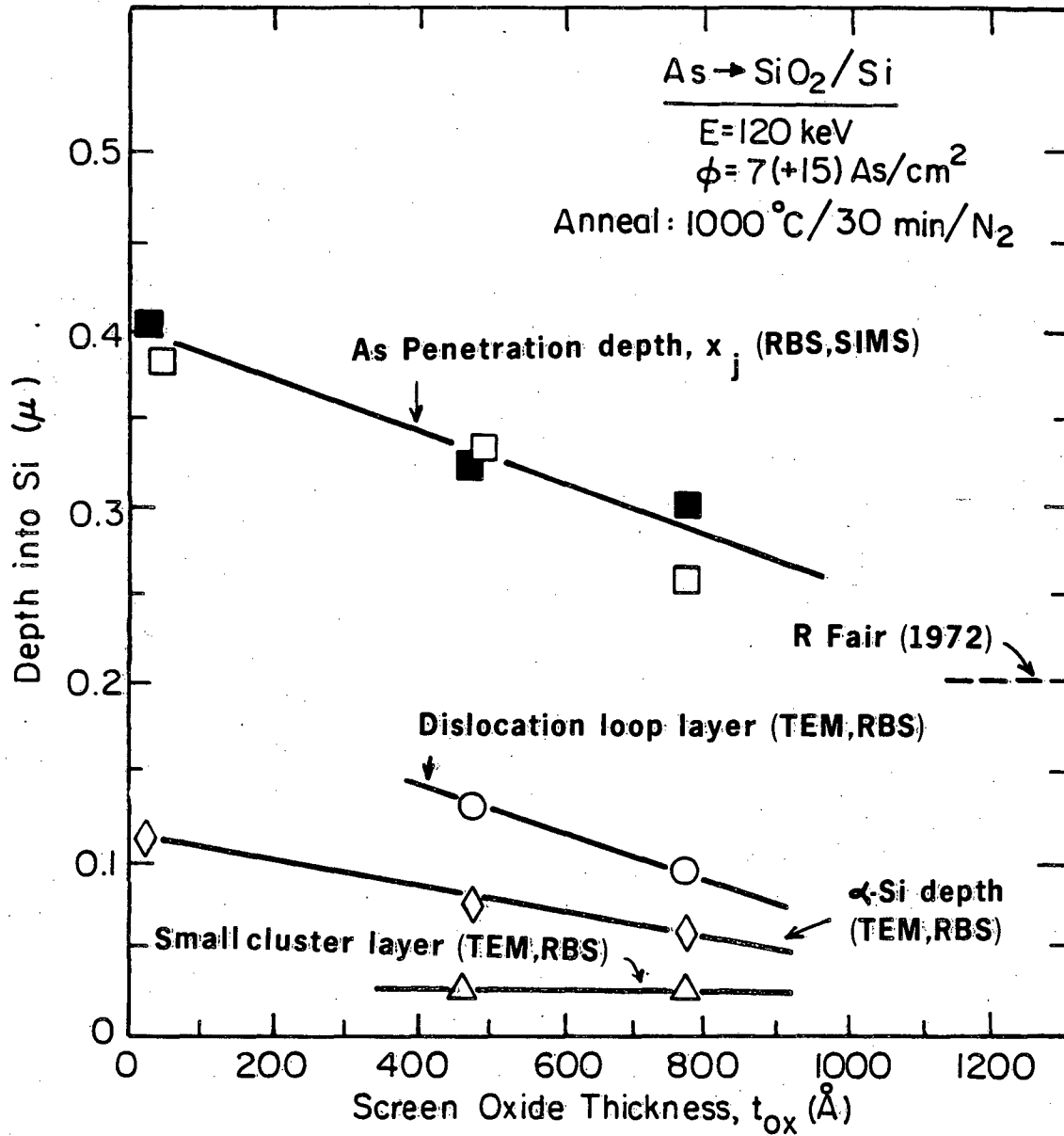


Fig. 6

XBL 828-6419

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