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The Mini-CAPTAIN Liquid Argon Time Projection Chamber

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Abstract

This manuscript describes the commissioning of the Mini-CAPTAIN liquid argon detector in a neutron beam at the Los Alamos Neutron Science Center (LANSCE), which led to a first measurement of high-energy neutron interactions in argon. The Mini-CAPTAIN detector consists of a Time Projection Chamber (TPC) with an accompanying photomultiplier tube (PMT) array sealed inside a liquid-argon-filled cryostat. The liquid argon is constantly purified and recirculated in a closed-loop cycle during operation. The specifications and assembly of the detector subsystems and an overview of their performance in a neutron beam are reported.

Keywords: Liquid argon detector, time projection chamber, neutron measurement, photon detection system

1. Introduction

A key aspect of long-baseline neutrino oscillation measurements is the ability to infer the neutrino flavor (e , μ , or τ) and type (ν or $\bar{\nu}$). This requires clear identification of the outgoing lepton coming from the neutrino interaction, such as e^- or e^+ in the case of a ν_e or $\bar{\nu}_e$. With this in mind, liquid argon time-projection chambers (LArTPCs) provide several advantages over other detectors, like those based on water Cherenkov technology. A combination of dE/dx differentiation

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and high-resolution track topology of final state particles in LArTPCs allow for improved particle identification, thereby suppressing backgrounds. ICARUS was one of the first experiments to successfully demonstrate the advantages of using a LArTPC [1].

The Cryogenic Apparatus for Precision Tests of Argon Interactions with Neutrinos (CAPTAIN) project was conceived to investigate the technical challenges of developing large-volume LArTPCs and explore their physics capabilities [2]. Mini-CAPTAIN is a first prototype of CAPTAIN and it made a first measurement of the high-energy neutron cross-sections and event signatures in a LArTPC [3]. In addition to a cross-section measurement, Mini-CAPTAIN also tested a number of hardware advancements for future LArTPCs. The hardware advancements consisted of new TPC cold electronics developed by Brookhaven National Lab (BNL) for the MicroBooNE detector, a prototype hexagonal time-projection chamber (TPC) designed for the larger CAPTAIN detector, a chemically-etched cathode grid plane, and several configurations for injecting high-power laser light into the TPC fiducial volume. This report details the specifications and assembly of the detector subsystems and provides an overview of their performance in a neutron beam.

2. Mini-CAPTAIN Detector

Mini-CAPTAIN consists of a hexagonal TPC with a 50 cm apothem and a 32 cm drift distance corresponding to a total active liquid argon mass of 400 kg. A high-voltage cathode wire plane is used to apply an electric field of 500 V/cm across the active volume of the TPC which corresponds to an electron drift velocity of 1.6 mm/ μ s. Ionized electrons drift away from the cathode plane and towards a series of sensing wire planes where they form signals for readout. Accompanying the TPC is a photon detection system (PDS) consisting of an array of photomultiplier tubes (PMTs). The PMTs view the active region of the TPC through wavelength shifting plates positioned below the cathode plane (looking up) and positioned above the wire sensing planes (looking down). The PDS is designed to collect the vacuum ultraviolet scintillation light in conjunction with the ionized charge measured by the TPC. The addition of scintillation light aids in event reconstruction, provides timing for non-beam events related to cosmic rays or radioactivity from the surrounding environment, and provides a measurement of neutron kinetic energy via time-of-flight.

The TPC and PDS are contained inside a large cryostat. All instrumentation and cryogenic leads are fed

through the top lid of the cryostat. The flanges on the outside of the top lid allow access to the PDS, liquid argon purity monitor, and re-circulation heater. It also facilitates optical access to the liquid argon for laser calibration. The entire detector assembly was positioned on stationary blocks in the neutron beam line. An overview drawing of the Mini-CAPTAIN detector is shown in Figure 1. Extensive details and specifications of all of the Mini-CAPTAIN detector subsystems are described in the sections that follow.

2.1. Cryogenic Subsystems

Liquid argon has proven to be a cost-effective material to serve both as the target and detector medium [1, 4] due to its relatively large natural abundance and the ease at which it can be refrigerated with inexpensive liquid nitrogen. As a detector medium, charged particles traversing a liquid argon volume will leave behind trails of ionized electrons and produce an abundance of scintillation light at vacuum ultraviolet wavelengths. However, sufficient purity must be maintained in order to properly utilize these signals. Residual contamination in industrial liquid argon, such as nitrogen, water, and oxygen, can significantly impede the detector performance. For example, nitrogen degrades scintillation yield due to the addition of non-radiative processes that induce quenching. Oxygen and water are electronegative impurities and will capture ionized electrons, thereby greatly reducing their lifetime and average drift distance.

The TPC drift distance of 32 cm requires that the concentrations of H₂O and O₂ be less than 0.3 ppb and 1.5 ppb, respectively. The relative difference in H₂O and O₂ concentrations is due to the fact that with an electric field strength of 500 V/cm, H₂O has an electron attachment rate five times stronger than that of O₂ [5]. Studies have shown that to achieve optimal scintillation yield, the N₂ contamination must be smaller than 2 ppm such that quenching and absorption of scintillation light are negligible [6, 7]. The maximum allowable H₂O and O₂ concentrations are roughly three orders of magnitude lower than the contamination level of the industrial liquid argon delivered to Los Alamos (H₂O and O₂ concentrations of 0.5 and 2.3 ppm, respectively). Therefore, extensive purification is required.

The cryogenic systems comprise a cryostat, a gas recirculation, filtration and condensing system, and a criotec liquid purification system. During filling, the argon was passed through an inline filter. These subsystems are further discussed below.

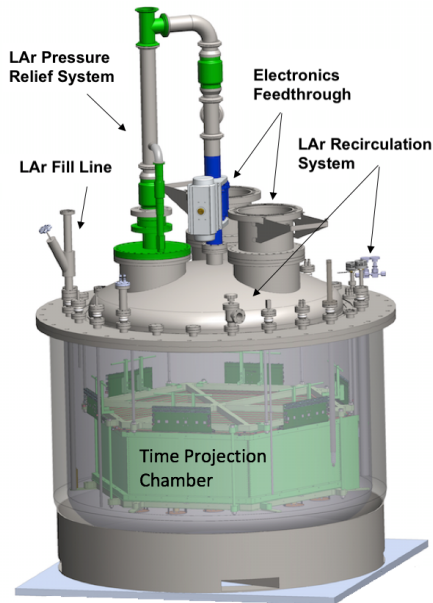


Figure 1: Overview of the Mini-CAPTAIN detector.

2.1.1. Cryostat

The cryostat is an evacuated dual-wall vessel made out of 304 stainless steel. It has a height of 1.47 m, an inner diameter of 1.40 m and an outer diameter of 1.86 m. The inner wall thickness is 0.16 cm and the outer wall thickness is 0.32 cm. The inner shell has a depth of 49 cm. Though the TPC hangs 18.6 cm from the top of the cryostat, the liquid argon level was kept another 9 cm higher to ensure that the cold electronics (see Section 2.2), sitting on top of the TPC, were submerged.

The lid has multiple conflat flanges for the cryogenic systems, electronics feedthroughs, and laser calibration testing. The cryogenic ports included a fill line, a gas recirculation port, liquid return ports, and a pressure relief valve. There are two large conflat flanges for the TPC feedthroughs and twenty-four 2.75 inch conflat flanges used for various systems such as the photon detection system and high voltage feedthrough. Two additional ports for a laser calibration system were positioned between the two TPC feedthrough conflat. A heater was also installed for control of the boil-off rate and tank pressure. Figure 2 shows Mini-CAPTAIN before its final deployment into the WNR facility of the LANSCE accelerator.

Two important factors impacting the purification of liquid argon are the cryostat leak rate and the outgassing of internal detector components. The purification system must remove impurities at a rate exceeding the introduction of contaminants from system leaks and com-

ponent outgassing. Leaking can be reduced with proper sealing of the cryostat. Proper selection of material used inside the detector can reduce the outgassing of oxygen and water. Diminished purity due to outgassing was further mitigated by implementing alternating cycles of vacuum pumping followed by argon gas purging before the cryostat is filled with liquid argon. During this cycling period, the cryostat was evacuated to a level better than 10^{-5} Torr before being purged with pure argon gas.

2.1.2. Liquid Argon Inline Filter System

During the initial filling of the cryostat, liquid argon is passed through an inline filter. It has two active components: a molecular sieve to remove H_2O , and activated copper for removal of O_2 . The inline filter is regenerated after each fill of the cryostat. As can be seen in Figure 3, the filter is aligned in an upright position, such that the liquid argon is fed in through the top and extracted from the bottom. During regeneration, gaseous argon is circulated through the filter in the op-



Figure 2: The Mini-CAPTAIN cryostat during its deployment into the WNR facility at the LANSCE accelerator facility. The TPC is suspended from the top head of the cryostat and the cold cables are shown connecting the motherboards and wire planes to the service boards on top of the lid.

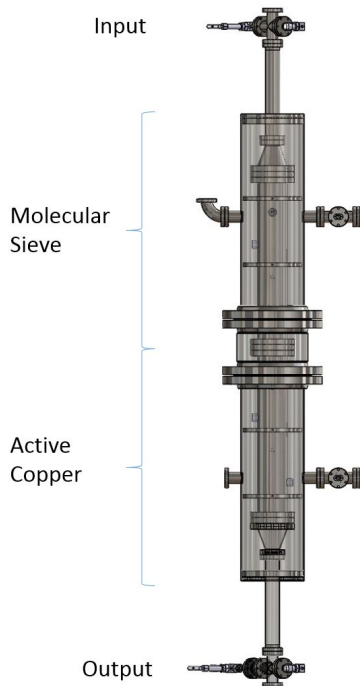


Figure 3: The Mini-CAPTAIN inline filter used for initial filling of liquid argon into the detector.

posite direction. The O_2 concentration is measured with a DF-550E Process Oxygen Analyzer from Servomex Company Inc., and the H_2O concentration is measured with a Continuous Wave CRDS Trace Gas Analyzer from Tiger Optics, LLC. These instruments sample the evaporated argon gas after the inline filter and have sensitivities for O_2 and H_2O of 3 ppb and 1 ppb, respectively. The inline filter was observed to lower the O_2 concentration to 400 ppb and the H_2O concentration to 500 ppb. A liquid recirculation system, described in the following section, was used further reduce the contaminants after filling.

2.1.3. Gaseous Argon Recirculation System

Further purification of the argon in the cryostat is provided by a gas recirculation system. The recirculation system collects gas from the cryostat ullage. The gas is passed through two parallel SAES getter cartridges where oxygen and water are extracted. Finally, the gas is then fed into the condenser system at the top of the recirculation tower before returning cryostat.

The condenser was designed by Dr. Hanguo Wang at UCLA. It consists of 19 copper tubes brazed with stainless steel, which have different lengths. By varying the flow of N_2 the depth in each tube is controlled

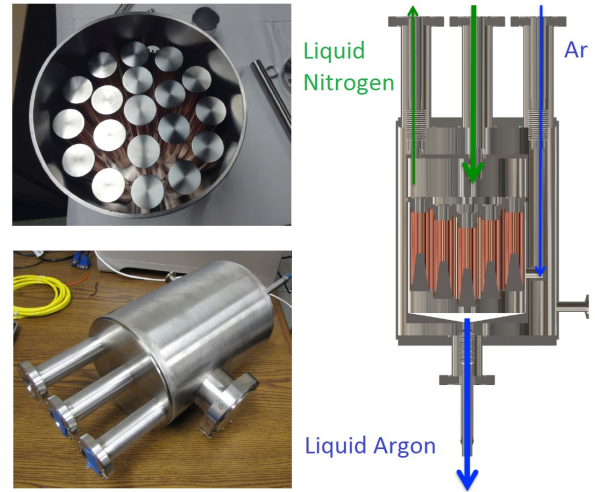


Figure 4: Two images of the liquid argon condenser system for Mini-CAPTAIN are shown on the left. A diagram of the general flow of argon through the condenser is shown on the right.

allowing the cold surface area in contact to vary. The evaporated argon gas flows into the top, where it runs between the tubes cooled by liquid nitrogen. After heat is transferred, the condensed argon drips to the bottom and falls back into the cryostat. Temperature sensors were used on the nitrogen input and output to observe the heat transfer during operation. Two images showing the inside and outside of the condenser on the left side of Figure 4. A diagram depicting the flow of gaseous argon into the top of the condenser and flow of the liquid leaving the condenser is shown on the right side of Figure 4.

During early stages of liquid argon filling, achieving the required liquid argon purity proved challenging, particularly due to lingering oxygen contamination. The previously described system ultimately achieved contamination levels of 3 ppb for O_2 and 1.5 ppb for H_2O . The corresponding electron lifetime was deemed insufficient.

2.1.4. Criotec Filtration System

After tests demonstrated that the inline filter and recirculation system could not achieve the necessary purity, a new liquid argon purification system was procured from Criotec Impianti [8]. The system was designed with two purifier cylinders made with sintered copper-alumina, shown in Figure 5, and is attached directly underneath the TPC. Liquid argon is cycled through the filter via the pressurization of two bellows pumps, which is achieved using a slight over-pressure of nitrogen gas. Two pneumatic timers alternate control

over a 5/2 valve to feed the nitrogen over-pressure to the pumps. During the late stages of commissioning the Criotec filtration system brought the O₂ contamination level down to approximately 1 ppb.

2.2. Time Projection Chamber

The hexagonal LArTPC consists of four primary components: a cathode plane, a field cage, a stack of wire planes, and a ground plane. The cathode plane was mounted to the bottom of the field cage, while a series of five wire planes were mounted to the top. Ordered along the direction of electron drift, the planes are labeled as: grid, u, v, x and ground. The u- and v-planes are rotated ± 60 degrees, respectively, relative to the anode x-plane. Each wire plane consists of 332 wires made of 75 μm diameter gold plated copper beryllium, with 3 mm inter-wire spacing. The planes are separated by a vertical distance of 3.125 mm. The wires are biased so that drifting electrons pass through the grid, u-, and v-planes unimpeded. The electrons are collected on the x-plane. Induced signals are sensed on the u and v wires. The grid plane is used to define the induced signal on the

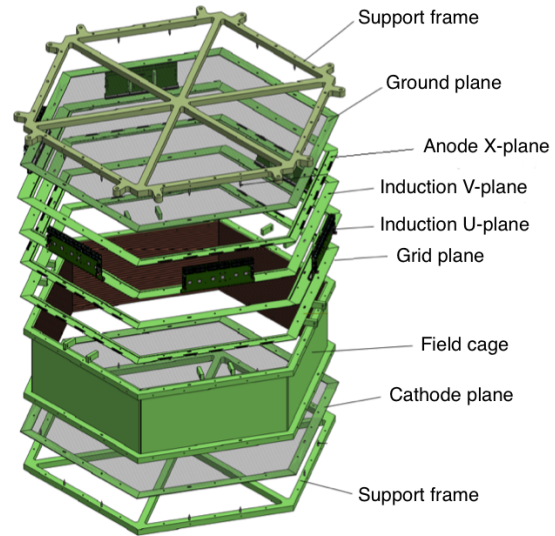


Figure 6: Diagram of the Mini-CAPTAIN TPC.

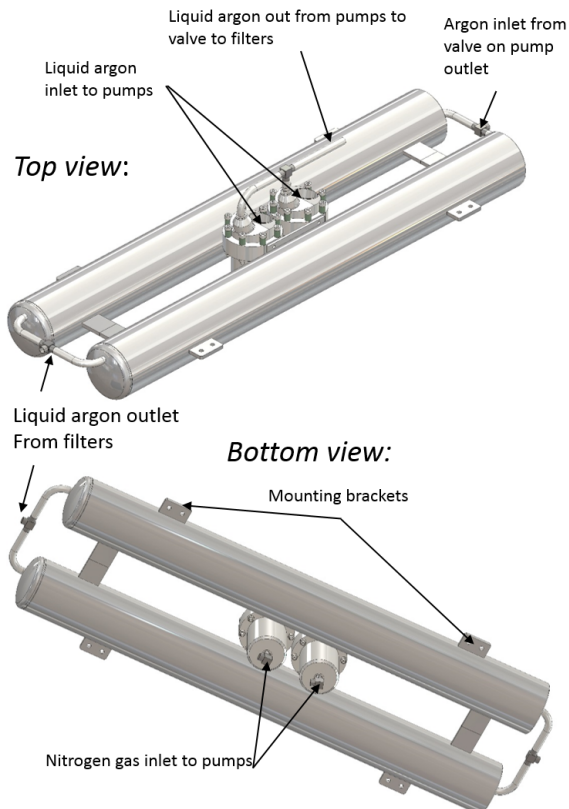


Figure 5: Diagram of the Criotec filtration system.

u-plane to be determined by the space between between the grid and v-plane.

The hexagonal field cage is constructed out of six copper clad FR4 sheets with a pattern on the inside of the cage that consists of 5-mm-wide strips separated by 10 mm running from the cathode end to the wire plane end. A resistive divider chain supplies high-voltage (HV) for each trace and gold-plated jumpers bridge the traces across each of the six cage corners. The resistive chain uses 25 M Ω /cm divisions, producing a gradient of 500 V/cm for 20 μA of current. The chamber's bolts and nuts are made from G-10 fiberglass laminate. Thick (2.54 cm) FR4 support frames held tension on the wire planes during assembly, and also bound the LArTPC components together as the assembly hung from the top head of the cryostat.

Two 1 cm \times 5 cm slits were milled through one face of the field cage, providing a window into the active volume for a light beam from a Quantel Brilliant B Q-switched Nd:YAG laser calibration source. The laser has a wavelength of 1064 nm and pulse energy of 850 mJ. Frequency doublers are used to shift the wavelength to 266 nm with a pulse energy of 90 mJ. The light is directed into the LArTPC using a periscope with an optical mirror in such a way as to span the LArTPC drift region, thereby creating well-defined ionization tracks within the LArTPC. During the commissioning process the laser produced ionization tracks which were measured by the LArTPC wire planes thereby providing additional confirmation that the required liquid argon pu-

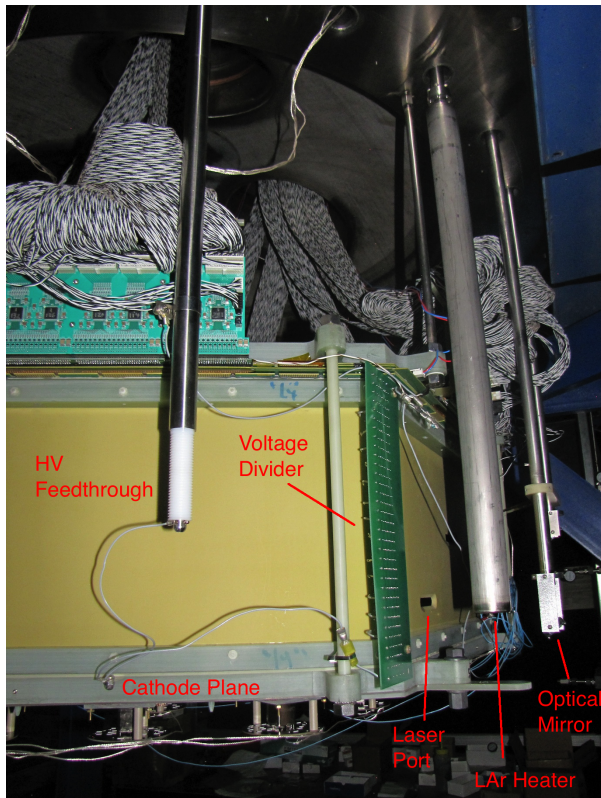


Figure 7: Close-up image showing the field cage assembly with the HV and calibration components labeled.

rity had been achieved.

A diagram of the LArTPC wire plan and field cage assembly is shown in Figure 6. An image showing the relative positions and assembly of the HV feedthrough, voltage divider, liquid argon heater, and laser calibration port is shown in Figure 7.

2.2.1. Cathode Plane

A copper clad FR4 sheet with PMT windows acts as the ground plane and is described in more detail in Section 2.6. The cathode plane is formed from two chemically etched trapezoidal sheets of stainless steel with 2.56 cm of overlap between them. To the best of our knowledge, this is the first successful demonstration of a chemically etched cathode plane instrumented inside a TPC. The high voltage feedthrough held the cathode plane and voltage divider chain at -18 kV. A Matsusada linear power supply provided a voltage from -14 kV to -19 kV, with currents around 20 mA. Oscilloscope traces showed peak-to-peak variation of 10 mV in the HV. A drawing and close-up image of the chemically etched grid on the cathode plane is shown in Figure 8.

2.2.2. Wire Planes

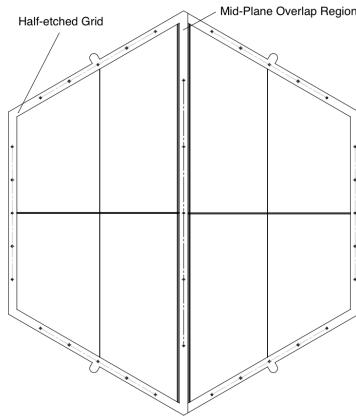
The stack of wire planes consists of a so-called ‘grid’ wire plane and three ‘sensing’ wire planes that produce bipolar induction signals as ionized electrons pass by. All three sensing wire planes are identical in their construction and framed by laminated copper clad FR4 receiver boards. The gold-plated copper beryllium wires, ranging from 0.5 m to 1 m in length, were wound with a target tension of 130 gram-force. This tension was verified by mechanical oscillations in each wire induced by pulsing current through wires in the presence of a magnetic field [9]. Measured tensions were between 90 to 130 gram-force. The wire-mesh ground plane sits above the stack. The grid plane defines the electric field in the sensitive drift volume for the u-plane. Specifically, the grid plane masks the u-plane from the field of the approaching ionization electrons. This ensures that the electrons pass the u-plane and the following v-plane while inducing the same bipolar peaks on both planes. Additionally, the grid plane enables clear determination of the midpoint of each bipolar signal, and eases hit-finding on any channels exhibiting heightened noise levels.

The grid, u-, and v-planes are held at bias voltages (relative to ground) of -420 V, -230 V and 0 V, respectively, while the collection x-plane was held at $+270$ V. The ground plane was maintained at the high-voltage ground. Bias power to the planes is delivered directly to the receiver boards. This power is distributed to the wires through a resistor chain with capacitive coupling to the readout connector. The bias voltage is carried to each grid, wire and ground plane from the feedthrough boards using teflon coated wires. The DC component is decoupled with a capacitor-resistor pair just after the wire attachment point. The receiver boards have 72-pin motherboard connectors.

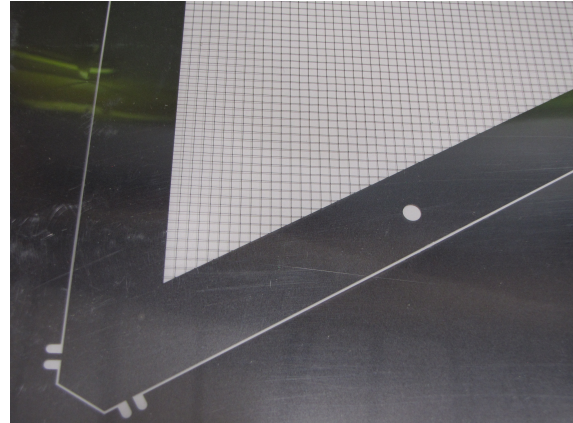
2.3. LArTPC Readout Electronics

The readout electronics is made up of three subsystems: the front-end electronics (FEE) [10], back-end electronics (BEE) [11], and wire-plane data acquisition (DAQ) [12]. The word “front-end” denotes the electronics mounted directly on or inside the cryostat, e.g. the *in situ* motherboards sitting in the liquid argon. The word “back-end” denotes the triggering and signal processing systems sitting outside of the cryostat. A flow diagram of this system is shown in Figure 10.

An application-specific integrated circuit (ASIC) configuration board is used to communicate instructions to the cold motherboards. They consist of three digital input/output channels. A USB type-B cable delivers



(a) Diagram of Assembled Cathode



(b) Close-Up of Etched Cathode

Figure 8: A diagram of the assembled cathode plane is shown in (a) and a close-up image of the etched grid on stainless steel sheet is shown in (b).

computer instructions to an off-the-shelf I/O board (NI USB6501), which is driven by the ASIC configuration board. The digital serial configuration stream is delivered to the service card through a differential HDMI cable to the service board. Apart from ASIC initialization, the systems are independently operable.

To initialize the BEE, the DAQ computer uses the ASIC configuration board to issue an ASIC rise-time and gain configuration instruction to the FEE service card. The service card relays that signal to the ASICs, along with power. Once initialized, the motherboards maintain continuous collection of signals from the wire planes and forward those signals to intermediate amplifiers.

The primary responsibility of the BEE is the uptake of analog signals from the wire planes via the receiver ADC boards. After analog-to-digital (ADC) conversion, these signals are passed to the front-end module (FEM) card for processing. The FEM card is controlled by a clock, thus continuously sampling and recording the ADC output even in the absence of a signal. With-

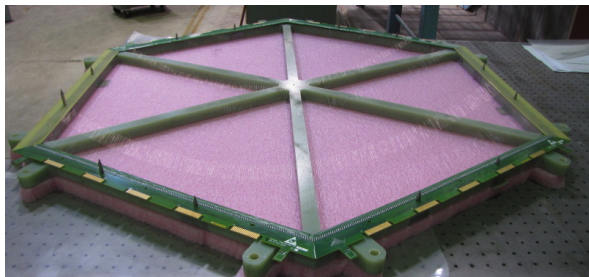


Figure 9: Image of a sense wire plane with copper beryllium wires spaced 3 mm apart on an FR4 frame.

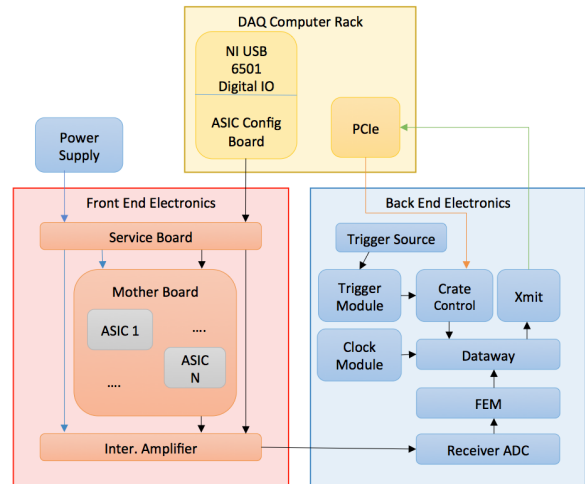


Figure 10: Flow diagram of the LArTPC readout electronics.

out a trigger, however, these data are dumped every cycle. On receipt of a trigger via the crate controller, a predetermined sample data size is taken from the FEM memory and passed to the transmit (XMIT) card. The DAQ computer then pulls the data from the XMIT card. The crate controller can be triggered from both the DAQ computer and the trigger module.

2.3.1. Front-End Electronics

Reliable and consistent data-taking in a LArTPC requires minimization of electronic noise. A thorough study of electronic noise in LArTPCs can be found in Reference [13]. The dominant forms of electronic noise come from first transistor noise, occurring during signal amplification process, and from thermal noise along

the sensing wires and connection leads. These forms of noise can be mitigated by minimizing the total capacitance of the sensing wires, signal cables, and input transistors. Cold front-end electronics, developed by Dr. Veljko Radeka and Dr. Hucheng Chen at Brookhaven National Lab (BNL), operate with a minimal path length between the signal wires and the preamplifier thereby reducing the total capacitance seen at the preamplifier input.

Thermal noise from the sense wires was mitigated by the use of copper beryllium material. The thermal noise was calculated based on the transmission line model of the sense wires as described in Reference [13] and was not a noticeable problem during data-taking.

Three large conflat flanges are instrumented atop the cryostat. Two of these flanges are used for front-end electronics feedthrough. Of all the FEE components, only the motherboards sit inside the liquid argon environment, connected directly to the termination/receiver boards on the wire planes. Cold cables run from the motherboards out to “feedthrough boards” atop the corresponding conflat. These receive power and configuration information from the service board, and return the wire-plane signals. The remaining FEE components sit inside Faraday boxes on each of the feedthrough circuit boards.

Each feedthrough board supports two columns of line driver cards, with each column managed by one service board (developed by BNL). Bus boards run along the side of the service board and intermediate amplifiers (line drivers), supplying power. The service boards supply approximately ± 1.8 V to the front end ASIC chips on the motherboards, and can also supply calibration pulses. Each board contains 1 molex power connector, 2 USB (type-A) ports for injecting the calibration pulse, and 2 HDMI configuration ports. An Agilent E3648A supplied ± 3.3 V to the power connector.

The cold motherboards, also developed by BNL, are designed to operate in liquid argon. Each motherboard utilizes 12 ASIC chips (six per side), and each ASIC chip reads out 64 TPC wires. An image of the front and back side of the cold motherboard is shown in Figure 11. The primary purpose of the motherboards is to amplify and shape the wire output signal. Data-taking runs typically use a gain of 14.7 mV/fC for this purpose. The motherboards also pass calibration signals and injection pulses from the service card input to the ASIC chips. Optionally, bias can be supplied to the wires through the motherboard. However, the standard mode of operation is to apply bias directly to the wire planes.

Upon receiving signals from the motherboards, intermediate amplifiers split them differentially and apply a

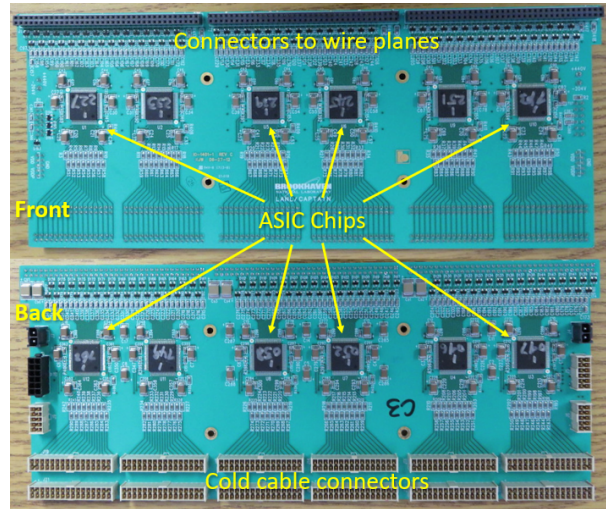


Figure 11: An image showing the front and back sides of a BNL cold motherboard.

gain of 12 dB. This ensures clean transmission of the analog signal to the BEE located 10 m away from the cryostat. A 68-pin SCSI-3 connector carries the differential signal from each of the 32 channel amplifier cards.

2.4. Back-End Electronics

The BEE consists of TPC VME crates, clock module, PDS VME crate, calibration fanout card, and power supplies [11]. The TPC VME crates were assembled and tested at Nevis Labs, at Columbia University. Each crate consists of TPC readout boards (mechanical assembly), crate controller, trigger module, XMIT card, dataway back-plane, and fanout card. Every crate is controlled and read out by a DAQ sub-event builder (SEB) computer, which passes data onto the main assembler computer.

Each TPC readout board receives data via two 32-channel differential warm cables. The board incorporates two sections into a VME 9U board: the digitizing section designed and built at Brookhaven National Lab, and the data handling section built by Nevis Labs. The digitizing section is laid out as an 8-layer printed circuit board. The ADC receiver board is a 14-layer printed circuit board, with an interface to the daughter Nevis front-end module (FEM) board as shown in Figure 12. More detail on the boards can be found in the following section.

2.4.1. ADC Receiver Board

The ADC receiver board incorporates 8-channel ADC chips (AD9222) and line receivers. The ADCs

sample at a rate of 2 MHz which is the optimum choice of the sampling frequency given the corresponding time scale of the electron drift velocity, wire plane spacing, and diffusion [14]. With low power consumption (less than 100 mW per channel) the AD9222 generates high integration density for the readout board. It also creates a high-speed low-voltage differential signal (LVDS) serial output stream. This reduced the number of FPGA connections required. To accommodate the various signal properties on the collection and induction planes, the collection plane signal baseline was made adjustable. The adjustments also ensure that the collection plane's unipolar differential signals could make use of the full ADC analog input range.

Two SCSI-3 connectors on each receiver board accept signals from 64 TPC wires. The receiver boards use four HM-Zd connectors to send the differential outputs to the FPGA chip. These connectors had individual ground shielding on each pair. The PDS triggers, clock signals, SPI signals, and light emitting diode (LED) signals were sent through a fifth HM-Zd connector. The receiver boards also includes a daughter FEM board on its backend described in the following section.

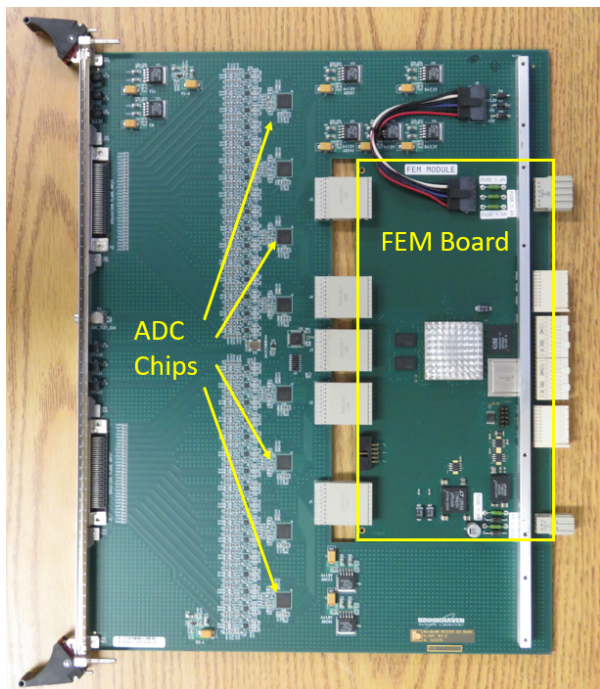


Figure 12: The mechanical assembly of the TPC readout board showing how the BNL and Nevis boards were joined together. The FEM daughter card fit into the ADC-receiver board.

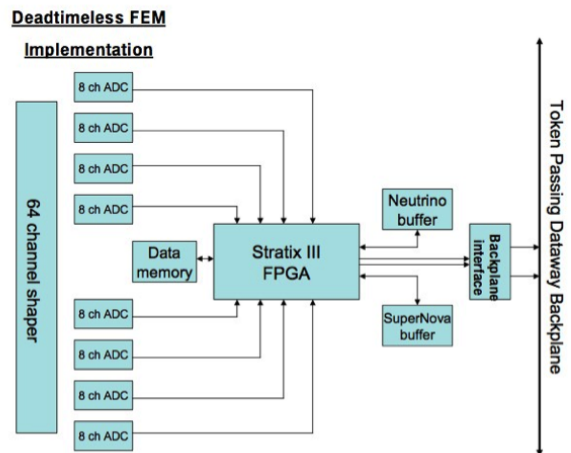


Figure 13: The digital readout scheme showing the 8 ADC chips, the FPGA, and the memory chips. Illustration taken from Ref. [12].

2.4.2. FEM Board

The data-processing chain begins with the front-end module (FEM) boards. Each FEM board includes a Stratix III Altera FPGA [12], which accepts the ADC-receiver board's digitized outputs as shown in Fig. 13. Each FPGA uses a $1\text{M} \times 36$ bit 128 MHz SRAM memory to save data from 64 wires sequentially in time. The data is grouped into two 36 bit ADC words. This requires a 64 MHz data storage rate ($64/2 \times 2$ MHz ADC sampling). The speed and size of the SRAM memory allows continuous readout of the TPC data.

The data-processing chain was configured to allow high uptake of data that coincide with beam-induced neutron interactions in the liquid argon. Accelerator beam bunches containing an interaction in the TPC drift volume were identified using the PDS to detect a prompt flash of light. A data frame header contained the beginning frame marker, the event type, the trigger position (if present), the channel address, and the word count. The readout of the clock is not synchronous with the neutron beam spill time. Consequently, each 4.8 ms window of relevant event data spans four 1.6 ms frames. To reduce the data rate, the FPGA trims the four frames to span the exact 4.8 ms required, with 1.6 ms being sampled before an event trigger and 3.2 ms afterward.

2.4.3. Trigger Board

The trigger board receives booster and WNR beam gate signals. The beam gate signals can be used to flag PDS triggers as neutron interaction candidates. PDS triggers that are valid can be compared with a logical OR operation to utility triggers received by the trigger

board thereby outputting calibration triggers. These calibration triggers can use various measurements including random triggers for noise measurements, off-beam triggers for cosmic ray response studies, and triggers for electronics studies.

2.4.4. Clock Module and Fan-out Cards

The clock module sends a low jitter signal from a 16 MHz clock to the two VME crates with ADC receiver boards [12]. The clock fan-out on the back of each crate distributed this timing signal to each TPC readout board. A local 16 MHz crystal on the crate clock fanout board generates a local clock for event gating (pretriggering).

The clock module was found to be sensitive to both the power source and ground. When the clock module occupied a NIM crate shared by other modules, the DAQ system would crash after a few hundreds of events. Isolation of the clock module into its own crate, with separate power and ground, reduces the frequency of DAQ crashes.

2.5. Feedthroughs

The signal transmission between the TPC and the readout electronics system consists of cold cables, warm cables, and signal feedthroughs. Gold plating on the exterior of the feedthrough board serve as the ground for the signals and power. The cold cables transmit detector signals from the cold motherboards to the intermediate amplifiers and also distribute power to analog front-end ASIC chips. The cold cable is a custom-built 32 twisted pair flat cable with Teflon FEP insulation. It used an AWG 26 solid core and silver plated conductors providing approximately $100\ \Omega$ impedance. The 64-pin termination uses 0.100"-pitch dual-row crimping. Correct alignment with the signal feedthrough pin carriers is ensured with custom shells and jack screws.

Signals between the intermediate amplifier and the receiver/ADC board on the readout crates are transmitted through 68-pin SCSI-3 warm cables, each with 34 differential pairs and two MicroDensity connectors. Shielding was fashioned from aluminum foil with 10% overlap.

The Faraday boxes mounted on top of the feedthrough boards shielded the intermediate amplifiers. One box also holds the four bias voltage feedthroughs, providing -430, -230, 0, and +230 V to the detector wire planes.

HV feedthrough enters the cryostat on a dedicated 2 3/4" conflat flange. The cable's own capacitance and a 25 M Ω resistor provides RC filtering. Figure 14 shows the canister used to shield the resistor, assembled with

a shielded feedthrough extension to deliver HV to the bottom of the TPC. An isolation standoff separates the flange from feedthrough, but the canister was eventually grounded to the cryostat during stable data-taking.

2.6. Photon Detection System

The PDS provides crucial improvements to neutron candidate event timing and energy reconstruction beyond what can be achieved by the LArTPC alone. Typical readout times for LArTPCs are on the order of one millisecond, whereas LAr scintillation light detection enables nanosecond timing. The projected nanosecond timing resolution is critical to allow separation of the prompt gamma-ray backgrounds from the subluminal neutrons during data-taking in the neutron beam. Furthermore, nanosecond timing resolution from the PDS allows the incident neutron energy spectrum to be reconstructed using time-of-flight.

2.6.1. Photomultiplier Tubes

The PDS is made up of twenty-four Hamamatsu R8520-506 MOD PMTs. The R8520 is a compact rectangular PMT which is approximately 1" \times 1" \times 1"

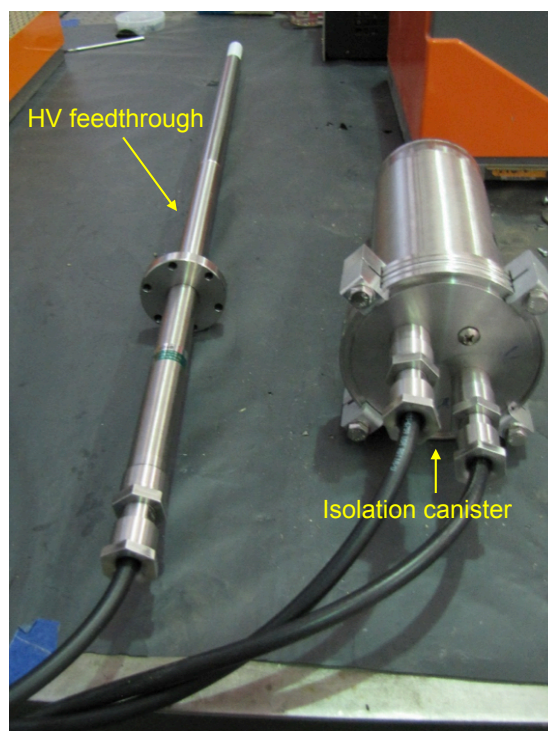


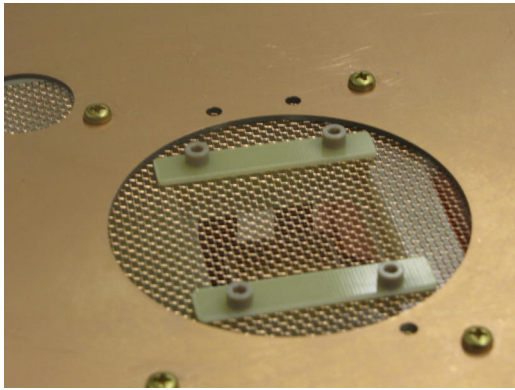
Figure 14: The HV feedthrough assembly with cable is shown on the left and the electrical isolation canister is shown on the right.



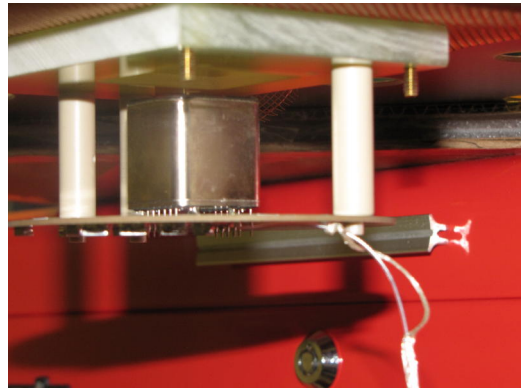
(a)



(b)



(c)



(d)

Figure 15: Images (a) and (b) show the locations of the PMTs with their bases mounted above the sense wire planes and on the bottom of a copper plate, respectively. Image (c) shows the copper grid just beneath the copper-clad FR4 plane and (d) shows a PMT and base sitting just beneath a TPB coated window.

in size. A borosilicate glass window exposes a special bialkali photocathode capable of operating at liquid argon temperatures (87 K). The R8520-506 MOD's peak quantum efficiency is 25% at 340 nm. Liquid argon scintillates at a VUV wavelength of 128 nm, which would be absorbed by the borosilicate PMT window before reaching the photocathode. Therefore, the VUV photons must be shifted towards the visible wavelengths before interacting in the PMT glass. This is accomplished positioning TPB-coated acrylic windows in front of the PMTs to shift VUV scintillation light into the visible wavelengths. Tetraphenyl butadiene (TPB) is the most commonly used wavelength shifter for liquid argon detectors and has a conversion efficiency of about 50% when evaporated in a thin film [15]. The re-emission spectrum of TPB peaks around 420 nm [16].

The PMTs are split into an array of 8 PMTs on the top plane and an array of 16 PMTs bottom plane. The top

plane is mounted on a frame just above the sense wire planes. The TPC ground plane, just above the sense wires, shield the PMTs from any residual TPC electric field. The bottom PMT plane is fixed behind the TPC cathode plane, with a copper-clad FR4 plane for electric field shielding. A copper grid sits between the copper-clad FR4 plane and the TPB coated window in front of each PMT. The positioning of PMTs relative to the TPC is shown in Figure 15.

2.6.2. Photon Detection Electronics

One Bertan 205A-03R supply powers all 24 of the PMTs. A positive bias of 800 V is daisy chained between three voltage distribution boxes and passed to signal pickoff boxes developed at UC Davis. The PMT bases were jointly designed by LANL and UC Davis. The pickoff boxes supplied power to each PMT base and decoupled the signal to the CAEN digitizers. The controller card passed commands from the DAQ to the dig-

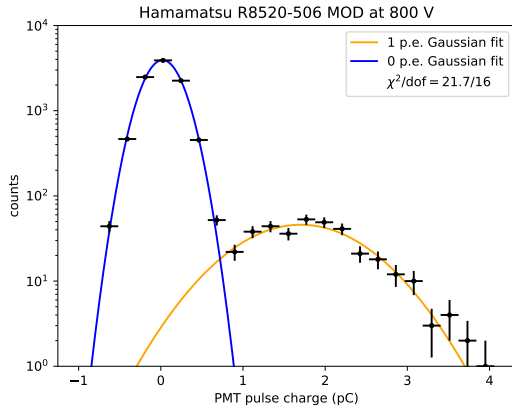


Figure 16: Typical PMT charge distribution measured at showing the single-photoelectron response at a bias of 800 V and less than 5% occupancy.

itizers, which triggered and recorded the signals. A typical single photoelectron charge distribution for a bias of 800 V is shown in Figure 16. The data was collected when the PMTs had less than 5% occupancy, and a peak-to-valley ratio of ~ 2.5 was observed.

Approximately 600 V was supplied to the PMTs during initial light leak checks after the cryostat was sealed shut. The largest source of light inside the cryostat was found to be the electrical and thermal isolation standoff on the top head. Significant light leaks were also found in all SHV voltage feedthroughs not fitted with a connector. Plastic and electrical tape were used to optically seal any suspected leak-points, and a removable cover was used to occlude the laser viewport. The PMT operating voltage was increased towards optimal levels as the light leaks were mitigated.

2.6.3. Photon Detection DAQ

PDS signals are digitized by three CAEN V1720 digitizers. The V1720 is an 8-channel, VME-based digitizer that samples at 250 MS per second (4 ns) at 12 bits. While Mini-CAPTAIN deployed 24 PMTs, only 21 PMTs are digitized in order to accommodate the additional RF synchronization pulse as input to each of the three V1720 digitizers. During initial testing, a VME crate controller was used to collect the data from the VME backplane and transmit to a DAQ computer via a single optical fiber uplink. This system was upgraded to an individual optical link between each digitizer board and the computer to greatly improve the data throughput for high-rate running conditions.

For the majority of data taking in the neutron beam, the digitizers were triggered externally. The first beam

pulse forced a zero-bias external trigger in both the PDS and TPC in order to maintain sub-system synchronization. After the first forced trigger, subsequent PDS triggers within the TPC acquisition window (~ 4 ms, to identify cosmic ray backgrounds) were driven by PDS activity. Each PMT channel had a threshold level set to ~ 0.25 photoelectrons. The V1720 has a free running level check that reports the number of threshold crossings on its analog front panel output: a voltage signal of $125 \text{ mV} \times [\text{number of level crossings}]$ is generated. These signals are fanned together in NIM-based analog electronics where a level discriminator selected the multiplicity of simultaneous PMT hits. For the neutron beam data taking runs, 4 PMT hits are required to trigger TPC data acquisition.

Approximately $8 \mu\text{s}$ of digitized data was collected per trigger, which sufficed to extract nearly all of the singlet and triplet light. Data was read from the digitizer boards and stored in shared memory. A second, asynchronous program pulled these data from shared memory and reconstructed the events in a ROOT tree [17]. Barring communication loss between the computer and digitizers, this created a dead-time free system.

The digitizer clocks for each board were not disciplined to an external oscillator because of the short duration of the neutron run. Instead of a GPS, the TPC and PDS computers were synchronized through an NTP timeserver. This provided ~ 10 ms timing synchronization between the PDS and TPC triggers, which was sufficient given that a 200 ms hold-off was used between beam triggers. The relative alignment of each digitizer clock does drift over the course of a run, but the alignment of the digitizer clocks is easily achieved with common event spacing, trigger multiplicity, and the presence of the RF synchronization pulse. A GPS module was used to assign a time-stamp to each PDS event.

2.7. Power Distribution and LED Testing

LEDs are installed on each end of the field cage to enable testing of the PMTs during the purification of the liquid argon. Figure 17 shows the results from the initial PDS test before moving Mini-CAPTAIN to the WNR neutron beam facility. There was some initial concern with the response on the 11th PMT, but later tests showed the PMT yielded signals comparable to those from the other tubes in response to neutron events. It is likely that the TPC support frame shadowed the direct line of sight from the calibration LED to PMT 11 as can be seen in Figure 17(a).

The LED system is also used to calibrate the single photoelectron response for each PMT. Since the PMTs are biased with positive high voltage, the photocathodes

in liquid argon are at the ground potential. The readout, sitting outside of the cryostat, requires capacitive coupling, which introduces slight overshoots and ringing. LED calibrations are used to measure the impulse response of the PMT and later correct for these artifacts.

3. Mini-CAPTAIN Commissioning

The commissioning campaign can be divided into three stages: warm cryostat testing, liquid nitrogen testing, and liquid argon testing. Initial warm tests only used the front-end electronics. During these initial tests, oscilloscope signals were primarily used. Some tests were also performed using Nevis Labs FEM readout software [10]. Before cold testing began, a complete end-to-end characterization of the electronics chain was made both in atmosphere and in vacuum. For these measurements, the high voltage was left off to prevent arcing.

3.1. Power Isolation

Mini-CAPTAIN was staged in a building at the end of LANSCE Experimental Area A. The staging area had so-called clean and dirty sources of power. Tests showed the clean power to be only slightly more stable when compared with the dirty power. Regardless, efforts were made to run sensitive electronics on clean power only. Additional isolation transformers were used whenever possible to suppress noise from the power source. The cryogenic and vacuum systems ran on dirty power and 3-phase power. The DAQ computers ran on dirty 120 V outlets. The front-end and back-end electronics racks were both isolated using separate transformers. To further increase the stability of the DAQ, the clock module was connected to its own clean power source.

3.2. Motherboard Operation

During initial testing, most of the calibration tests used a 200 mV injection pulse. However, some of the very first calibrations used ~ 500 mV pulse heights similar to the 600 mV pulse heights used by Nevis Lab [18]. It was later determined that continuous use of the ~ 500 mV injection pulses was harmful to the motherboards. This was evident every time the calibration testing initiated and a pair of motherboards on a wire plane would no longer configure. Reasonable caution was used during the electronics assembly and it seemed unlikely that the damage came from electrostatic discharge. To avoid further issues the 200 mV maximum pulse height was established, and in most

cases 80 mV was used. To further reduce systematic uncertainty during calibrations only specifically trained individuals were allowed to perform the calibration tests. After these additional measures were taken no further motherboard malfunction was observed.

3.3. High Voltage Operation

Prior to the liquid argon fill, it was necessary to test the electronics with the HV applied to the TPC. At this point, the cryostat was under vacuum. After the HV was turned on, and allowed to increase to -15 kV, the system tripped. The anode plane motherboards no longer configured properly, and there was a clear loss of 20% of the ASIC channels on the u- and v-planes. The most likely cause of these problems appeared to be HV discharge in the ground plane that in turn damaged the motherboards.

The HV discharge also damaged some of the decoupling capacitors on the wire plane. Near the end of the liquid argon commissioning run, a significant increase in the number of channels with high noise was observed. After opening the cryostat the wire current was measured across the decoupled capacitors on the respective channels. Up to 30% of the bias on the affected wires was leaking into the motherboards, with the worst being channel 593. The traces were cut for any wire with greater than 1 V of leakage.

Despite the damaged channels and subsequent repairs from the breakdown, the noise levels appeared low before the initial fill of liquid argon during the final liquid argon testing (an RMS around 5 ADC). However, after the HV was turned on again correlated noise across several channels near channel 593 was observed. The source and solution to this problem is addressed in the next section.

To reduce this risk of HV breakdown, isolating material was installed between the side of the cryostat and the cathode feedthrough tip, which limited the voltage allowed during the pre-fill testing. The motherboards were not replaced until wires with bad capacitors were isolated. These safeguards proved adequate in the subsequent data-taking run.

3.4. Time Projection Chamber Testing

Testing was performed in various TPC configurations, the most common configuration being an ASIC gain setting of 14 mV/FC, a rise time of 1 μ s, and 500 pA of leakage current. The bias voltage was usually applied, while the high voltage was left off (even when the cryostat was filled with a cryogen). Most of the noise sources were associated with the front-end electronics, so it was most efficient to measure the outputs

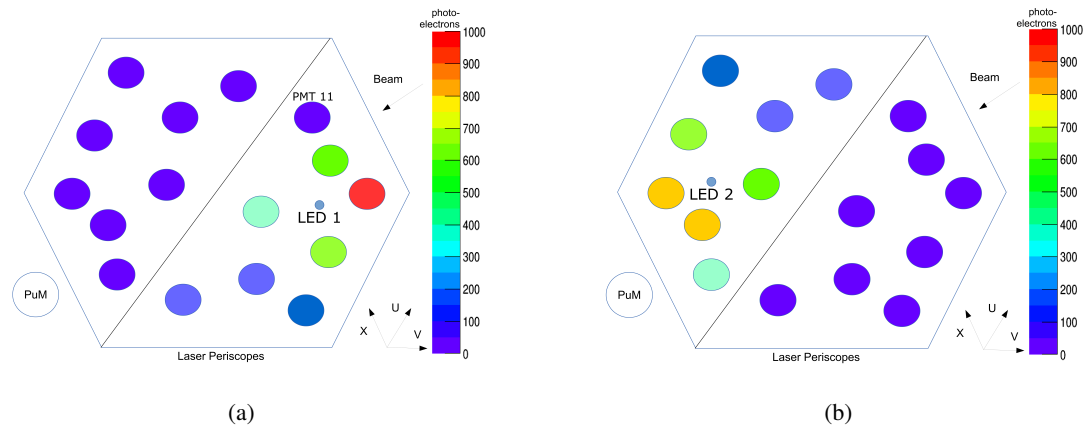


Figure 17: The PMT configuration for the PDS system, and the system's response to a calibration LED mounted on the (a) right or (b) left side of the field cage (looking downward).

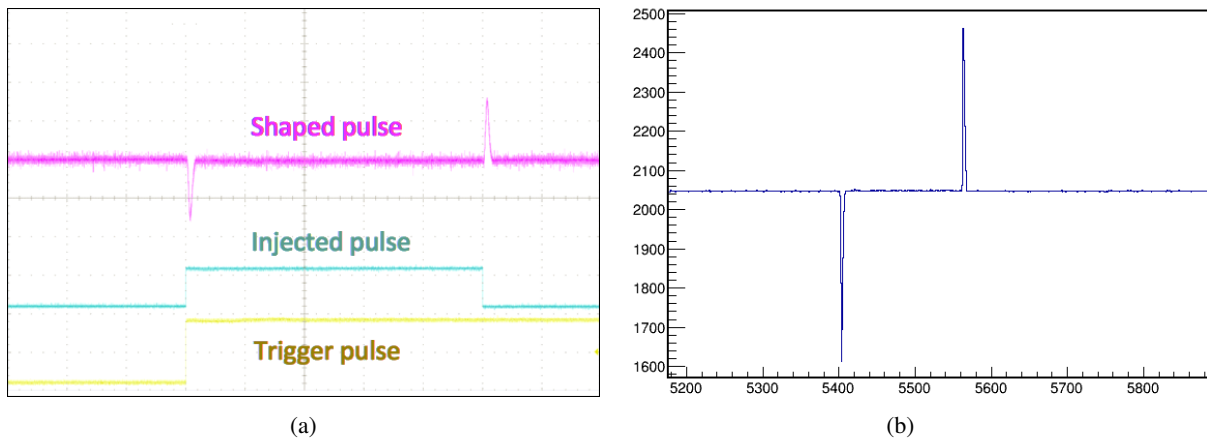


Figure 18: The calibration injection pulse after the shaping capacitor for is shown as an oscilloscope trace, including the original pulse and trigger, in (a). The final DAQ output of the calibration pulse is shown in (b).

of the line drivers with an oscilloscope. Figure 18 shows an example of the injection pulse used for testing. In this example, a 200 mV logic pulse with a width of 100 μ s is used. The input capacitor alters the form of the square signal into bipolar peaks. The Nevis Labs FEM reads out the same signal to show the same bipolar peaks with a height of approximately ± 400 ADC units.

Through all stages of the electronics testing, the baseline per channel remained consistent irrespective of the gain setting and FEM card used. Baselines also remained reasonably stable among channels within the same event. Comparisons were made among events within the same run, as well as events from runs across different run cycles. Measurements with heavy noise can leave the impression of a baseline shift, depending on the sampling range and the form of the background

signal. Smaller random pulses have the greatest effect on the code's sampling of the data. Some temporary shifts can be seen for a few milliseconds following a large noise spike.

During the first phase of liquid argon testing, channel saturation was observed on some events. That is, a random channel on any of the wire planes would lose its signal and produce a baseline profile with a fluctuation of only a few ADC units. Figure 19 shows an event recording where the saturation starts and another event recording where the normal waveform returns. The exact duration of the lockout is unknown, because the start and end of a given saturation period were usually not simultaneously recorded. There were also cases in which only the flatline was observed within the window, leading to the conclusion the saturation time could be at least

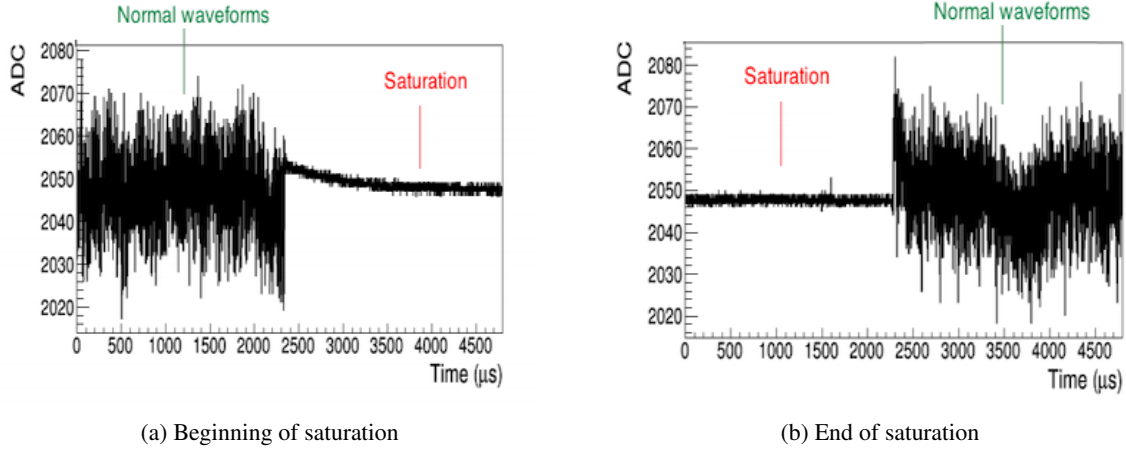


Figure 19: A normal waveform becoming saturated near the end of the $4.8\mu\text{s}$ acquisition window is shown in (a) and a saturated signal becoming unsaturated at the end of the window is shown in (b).

$4.8\mu\text{s}$ long.

ASIC response tests were performed on the test stand at LANL. The ASIC chips were found to be fairly robust in their motherboard configuration. Pulses were directly applied to the leads on the 72-pin connectors on the motherboards, where they had to pass through the protection diodes. The pulses ranged from 20 mV to 2.0 V, with short $20\mu\text{s}$ and long 200 ms widths. The pulse variation had no impact on the ASIC performance. However, the act of touching a wire to the connector would make the ASIC trip for short time. This duration was dependent on how long the wire was in contact. For contact lasting longer than 2 seconds, the lockout was approximately 52 seconds. For a contact lasting only a second or less, the channel would saturate for close to 10 seconds. No variation was seen with change of the ASIC configuration settings. A capacitor greater than 10 pF in series prevented the lockouts on the test stand.

A more complete study was performed at BNL [19]. They found that this effect can be significantly reduced by configuring the ASIC current bias to 500 pA. The saturation requires the positive current (I_{pos}) to exceed the internal bias current and the product of $I_{\text{pos}} \cdot \Delta t_{\text{pos}} / C_F$. The saturation can be avoided if the ripple, given by $V_{\text{ripple}} / \Delta t_{\text{pos}} \approx I_{\text{pos}} / C_F$, is less than 500 pA.

The problem only persisted for a few weeks, so there was little motivation to continue studying this phenomenon. During the first run cycle (October 2014), the lockout was observed to happen on one or two channels every few events. This saturation was no longer observed after significant noise reduction efforts were completed. For this reason, the ASIC current bias was

left at 100 pA.

The wire planes became the primary focus for noise elimination after the commissioning run cycle was complete. The traces were cut for wires with significant leakage and damaged motherboards were replaced. The new motherboards were tested on the test stand prior to installation to ensure all ASIC channels were read out properly. Some noise was still evident during the first neutron run. Prior to the second neutron measurement cycle, some additional traces were cut on the termination boards of the wire plane. The noise levels were sufficiently low for the remainder of the experiment.

4. Performance in a Neutron Beam

Mini-CAPTAIN measured neutron interactions in argon at the WNR facility at LANSCE, in a flight path known as 15R. The recirculation system, electronic racks, and computers were situated along the wall outside the beam path. Figure 20 shows the experimental setup within the WNR facility. Due to the long purification time of the liquid argon, the cryostat was initially filled and purified outside of the flight path enclosure. A few days before our neutron run, two of the shielding blocks shifted, and the Mini-CAPTAIN detector was moved into position on top of a rolling platform.

The first neutron run was performed in February 2016. It was clear from the start of data-taking that the purification system was not performing well. The ionized charged tracks observed by the TPC were not robust enough for any meaningful physics studies. However, this data proved useful for performance studies of the electronics. The data collected was also useful

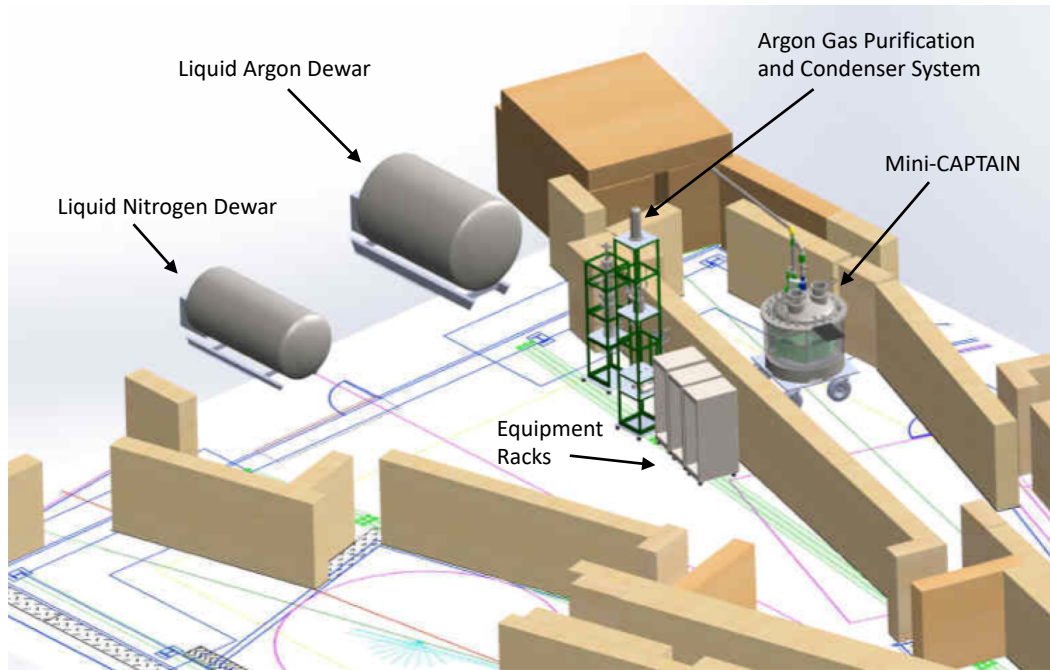


Figure 20: A diagram showing the experimental setup for neutron beam data-taking at the WNR facility, using the LANSCE accelerator. The neutron beam originates from the lower right corner of the figure, directed towards Mini-CAPTAIN.

for estimating the purity of the liquid argon, beyond the limits of the RTGs. Substantial improvement in the liquid argon purity was achieved by the second neutron measurement in May 2017.

There were two phases of neutron data-taking consisting of high intensity and low intensity runs. For the high-intensity runs, the beam was delivered at its usual intensity of $3.8 \mu\text{A}$. The shutter to flight path 15R was partially closed to reduce the rate of neutrons interacting inside the detector. During the first few hundred runs, a high neutron rate (order 10 Hz) was observed and the shutter opening was reduced further until an event rate under one neutron per second was achieved. On the last day of data taking, the accelerator operated at a low intensity with the shutter open for a dedicated period of time to deliver a nominal 1 Hz neutron interaction rate.

Flight path 15R is in a building that does not have a clean power source. However, it does have a large power distribution system installed, which provides significant improvement to the noise levels. Isolation transformers were used in a similar manner as was done during the commissioning process described in Section 3. Both the front-end and back-end electronics racks each had individual isolation transformers, while the clock-module was in its own crate, with an independent power source.

Following the same principles learned during com-

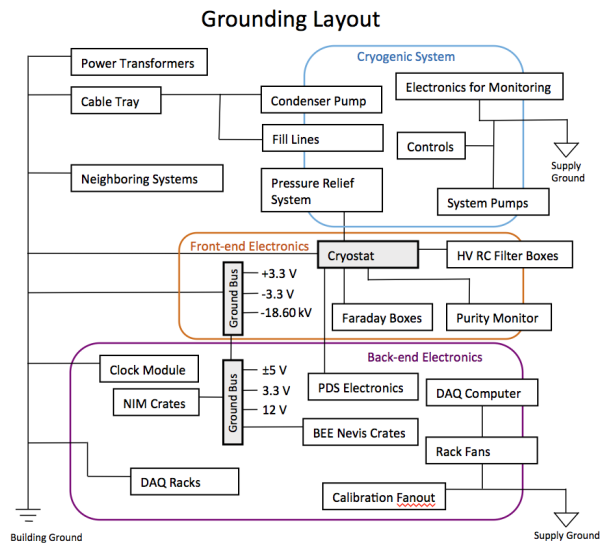


Figure 21: A flow diagram of the grounding layout for the experimental setup at the WNR facility.

missioning, the grounding of the front and back end electronics proved very effective and did not require much modification after the initial liquid argon filling. Some electronic noise did originate from the condenser pump sharing a stand that supports the detector cable

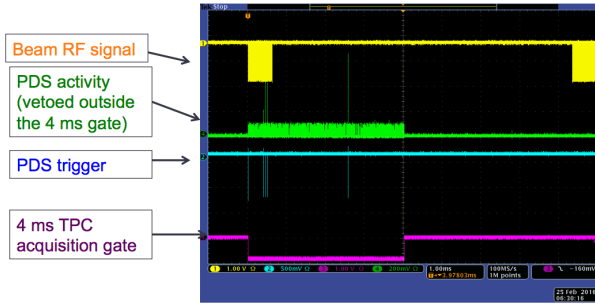


Figure 22: The traces of the beam RF signal, PDS activity, PDS trigger, and TPC acquisition gate as seen on the oscilloscope.

tray. Proper grounding of the roughing pump resolved this issue.

Figure 21 shows a flow chart outlining the final grounding scheme while taking data at the WNR facility at LANSCE. The initial experimental design had the cryostat and front-end electronics isolated from each other. However, after a series of noise studies it was deemed necessary to ground them together. The photon detection system and cryogenic plumbing and feedthroughs were also grounded to the cryostat. The back-end electronics were instead directly grounded to the building ground. It was sufficient to use the grounds of the power source for the computers, calibration fanout, cryogenic main pumps, purity monitors and rack fans. Some extra 2-inch braided straps were used on the neighboring systems (not used by the Mini-CAPTAIN detector), to include a single and three-phase transformer box.

4.1. Trigger Setup

The LANSCE proton beam is delivered in large bunches, called ‘macropulses’, each containing approximately 347 smaller ‘micropulses’. Each macropulse is separated by approximately 8.3 ms while individual micropulses are separated by 1.8 μ s. The macropulse has a repetition rate of 100 Hz. The TPC is forced to trigger on the first micropulse in a macropulse, and the TPC DAQ collects data for the duration of macropulse. After each TPC trigger, subsequent triggers are vetoed for at least half a second, and as long as two seconds, depending on a predetermined DAQ trigger rate. Figure 22 illustrates the timing between the beam RF signal, PDS activity, PDS trigger, and TPC acquisition gate as seen on an oscilloscope. The frequency of PMT hits demonstrates that at any given time within the TPC DAQ gate at least one PMT is firing. However, few neutrons can make it to the detector with the shutter mostly closed,

and the resulting neutron interactions produce events with multiple PMT hits.

The PDS signals are fanned into the analog threshold discriminator. The threshold of each PMT signal was set to 2.5 mV (~ 4 p.e.), and the analog threshold discriminator threshold is set at 800 mV (corresponding to at least four PMTs firing). This trigger signal is distributed to the GPS and three PDS DAQ boards and is vetoed outside the 4 ms TPC acquisition window. The first four channels of each PDS DAQ board record the PMT signals, while the fifth channel receives the RF signal from the accelerator.

The PDS trigger is also sent to one of the channels on the TPC DAQ output. The NIM logic pulse from the trigger is stretched into a 50 μ s signal, which is converted into a differential signal through a NIM/ECL converter and fed to channel 28 on the x-plane. Then the signal is processed through the electronics backplane, where it is transformed into a bipolar pulse with a long tail. Whenever multiple triggers overlap they produce pileup signals. Both of these artifacts can be seen in Figure 23. This input signal is used to help align the PDS and TPS data streams in the offline analysis.

The triggering scheme is shown in Figure 24. The accelerator RF signal opened a 100 ms gate generator and triggers the TPC DAQ. Its signal is recorded on channel 5 on each of the PDS digitizers. With the 100 ms gate open, the trigger is sent to a 4 ms gate. The fan-in-out takes the integrated charge from the individual PMT counters and passes them to the discriminator. If the summed value exceeds the threshold, the trigger is passed to the PDS boards and GPS for recording.

It was later determined that it might be challenging to align the TPC and PDS data, due to timing resolution between DAQ machines. Halfway through the data collection, a new triggering scheme was employed where the PDS trigger for record was shared with one of the TPC’s digital channels. This signal is passed through a 50 μ s gate generator in coincidence with the trigger to the TPC DAQ.

4.2. Neutron Flux Monitoring

The initial neutron flux was measured with a plastic scintillator detector before positioning Mini-CAPTAIN in the neutron beam. Measurements were taken with the shutter both completely open and partially shut. Figure 25 shows the flux with the shutters mostly closed for reduced neutron rate. The hits in the plastic scintillator detector coinciding with the neutron beam are plotted as a function of $(t_{\text{beam}} - t)$, where t_{beam} marks arrival of the neutron beam trigger signal at the digitizer. Note that this parametrization reverses the direction of the time

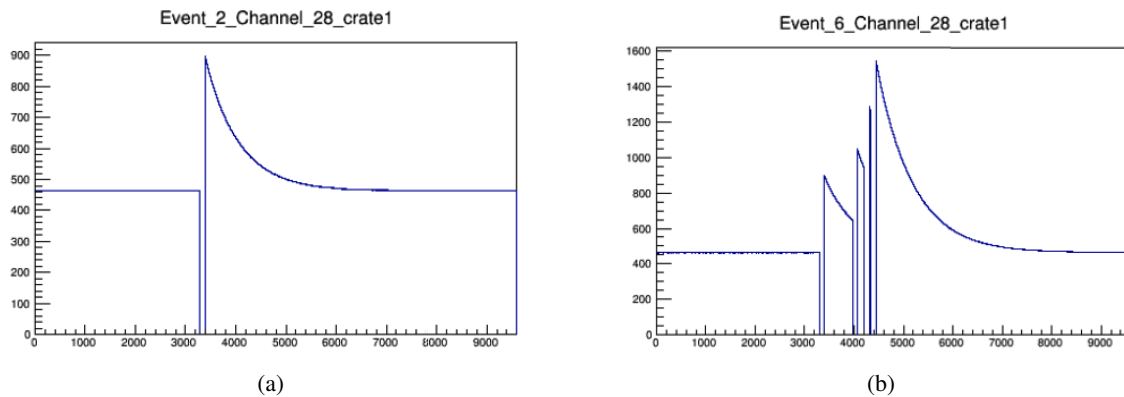


Figure 23: TPC waveform readout of a single injected PDS trigger is shown in (a) and a similar event with four overlapping triggers is shown in (b).

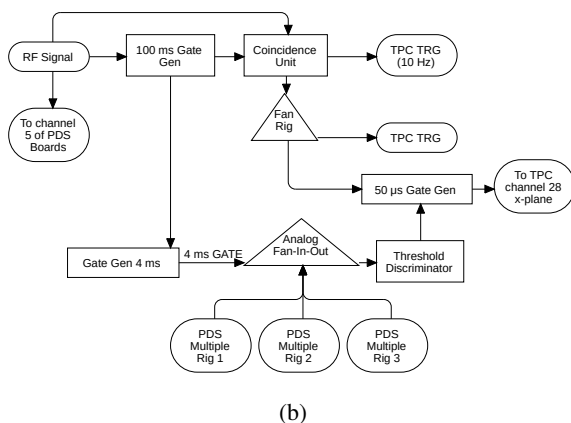
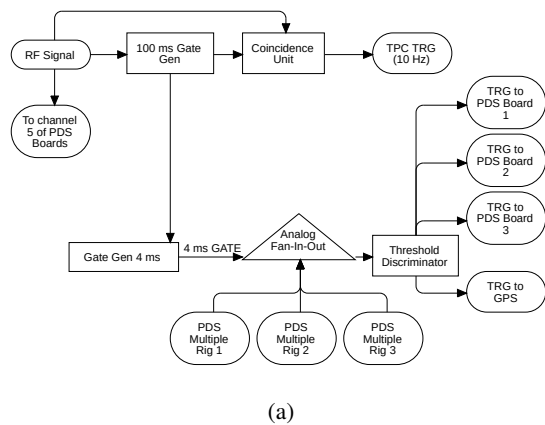


Figure 24: Logical flow diagram of the initial trigger design is shown in (a) and the modified trigger design is shown in (b).

axis. The neutron flux was also measured during the entire Mini-CAPTAIN run cycle.

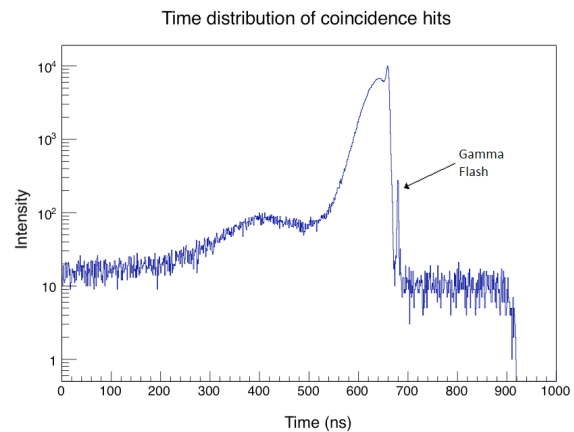


Figure 25: Neutron beam flux measured by a plastic scintillator as a function of $(t_{\text{beam}} - t)$.

4.3. Mini-CAPTAIN Detector Performance

The extensive effort to improve the liquid argon purity and minimize noise in the Mini-CAPTAIN detector subsystems allowed for a first measurement of the total neutron cross-section between 100 – 800 MeV. Data-taking in the LANSCE neutron beam with the detector running optimally yields clear evidence of neutron-induced ionization tracks in the TPC. These candidate neutron events are paired with their corresponding flashes of light observed by the PDS, thereby allowing a calculation of the energy through time-of-flight. Figure 26 shows several clear x-plane neutron interaction tracks observed in early data taking at the LANSCE neutron beam. The neutron beam enters the detector from the right side of the plot, the upstream direction corresponding to higher wire numbers. The amount of charge collected on individual wires at a given time is

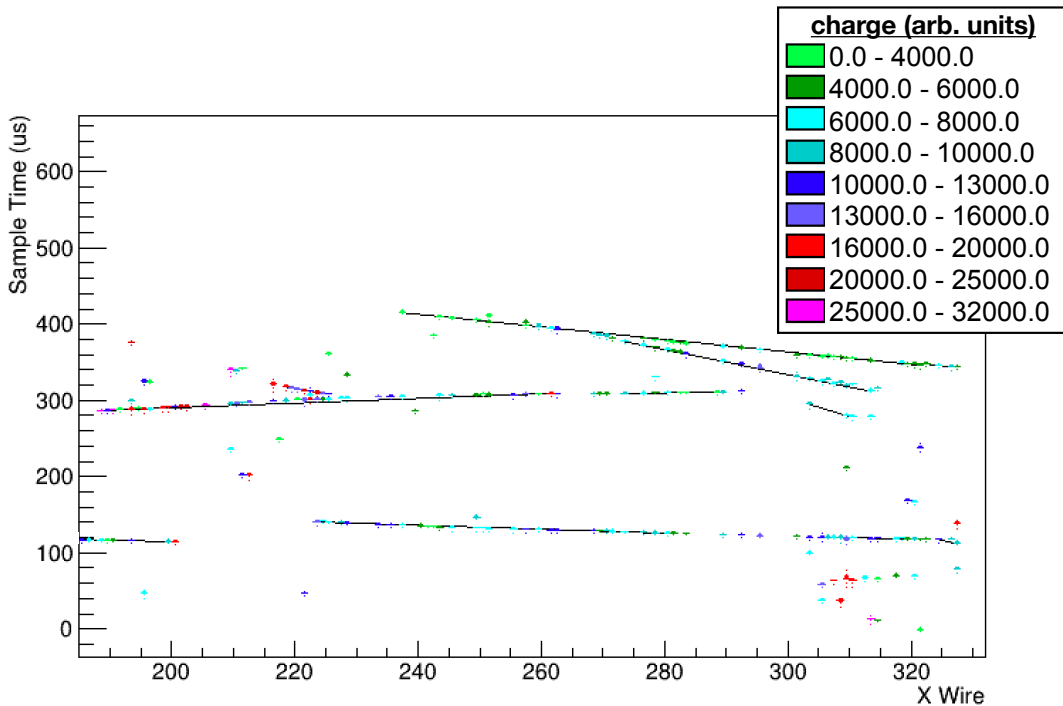


Figure 26: Neutron interaction candidate tracks observed with the x-plane of the Mini-CAPTAIN TPC in the LANCSE neutron beam at WNR. The color indicates the amount of charge collected on each wire at a given time.

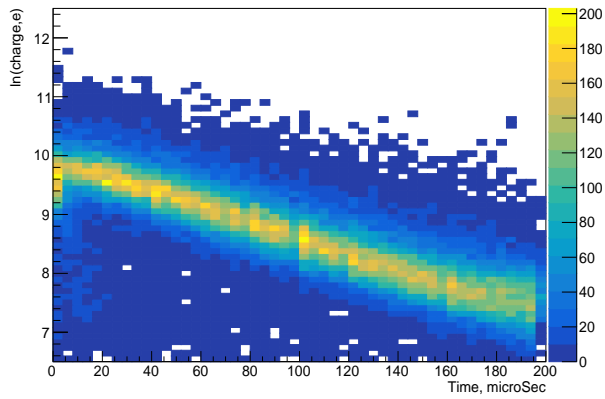
illustrated with an applied color scale as shown in the legend.

Reconstructed cosmic muon tracks spanning the entire TPC drift region, from the cathode plane to the induction planes, are used to estimate the wire collection efficiencies. This does not require the precision timing information offered by PDS, nor does it rely on the absolute time of a track. Instead the wire plane signals due to drifting electrons are measured as a function of the time between consecutive wire hits. The first wire observing a signal defines the starting time of the track readout period. Since each track is nearly 32 cm long, and the electron drift speed is $1.6 \text{ mm}/\mu\text{s}$ for a 500 V/cm applied field, the last wire hit for a single track arrives $200 \mu\text{s}$ after the first hit. Additionally, only tracks that crossed at least 30 wires in the x-plane were selected to ensure better track reconstruction. The wire signals for a track are measured over a $200 \mu\text{s}$ interval divided into 50 time bins, and the collected charge is calculated as the sum of charges of unique wire hits in each time bin. The muon track angle θ with respect to the wire planes includes a correction factor of $\cos(\theta)$ that is applied to the measured charge on each plane thereby providing absolute collected charge. The analysis was done separately for the collection wire plane (x) and the induction wire planes (u and v). The collected charge is plotted

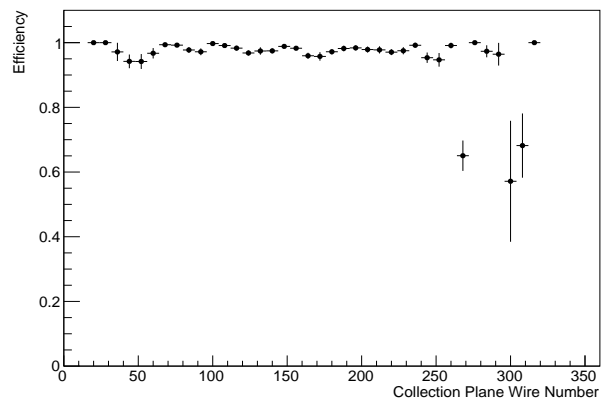
as a function of drift time for each plane in the left column of Figure 27. The collection of charge spanning the length of the drift region provided additional evidence that the liquid argon in the TPC was sufficiently purified.

Within the total drift time interval of $200 \mu\text{s}$, the window between $92\text{--}120 \mu\text{s}$ corresponds to the location of the neutron beam spot passing through the TPC. The wire collection efficiency was calculated inside the beam time window by selecting pairs of wire hits, one wire hit occurring immediately before the window and another hit occurring immediately after. Next, the total number of expected hits inside the time window, N_{pred} , is determined, and the predicted hit times of the wires in between the selected pairs is calculated using the electron drift speed. The actual observed number of wire hits inside the beam time window is denoted by N_{obs} . The wire collection efficiency is given by $\epsilon = N_{obs}/N_{pred}$. The collection efficiencies as a function of wire number are shown for each wire plane in the right column of Figure 27.

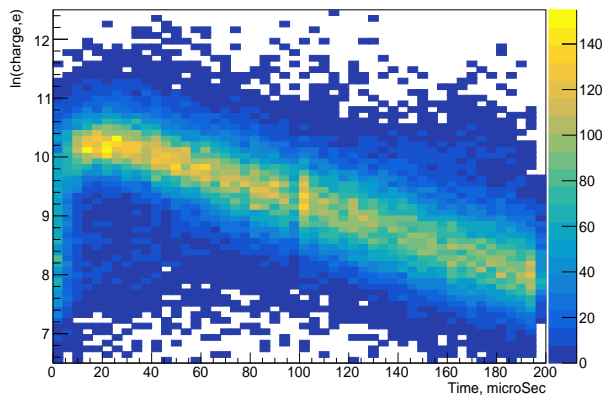
The efficiency study was critical for defining channels that could provide quality data during neutron data taking. The x-plane was observed to have 83 wires in the upstream region of the beam (increasing wire number) that did not meet data quality requirements, compared to



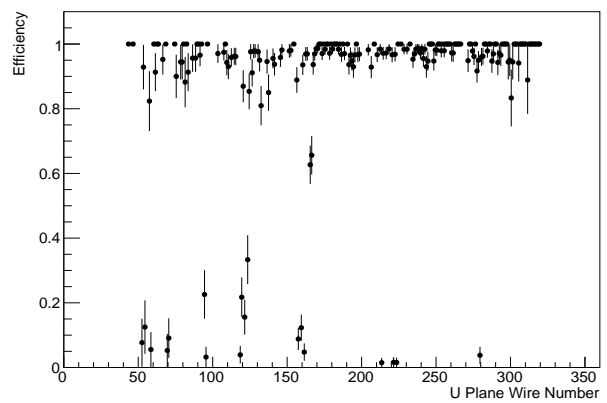
(a) Collected Charge vs Drift Time (x-plane)



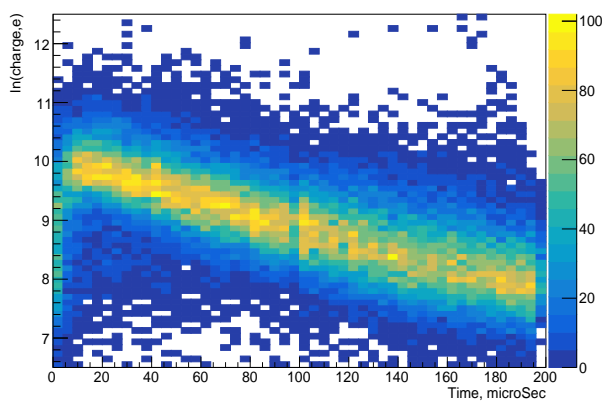
(b) Collection Efficiency vs Wire Number (x-plane)



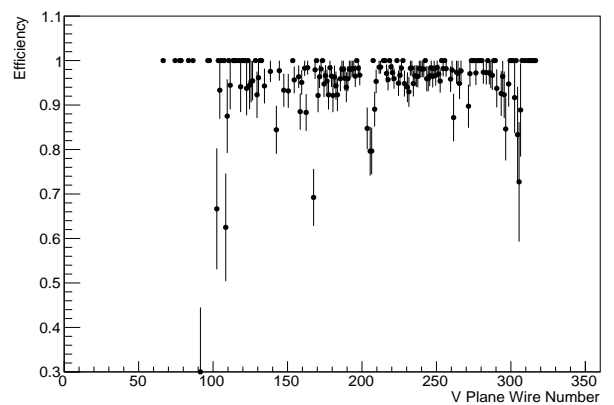
(c) Collected Charge vs Drift Time (u-plane)



(d) Collection Efficiency vs Wire Number (u-plane)



(e) Collected Charge vs Drift Time (v-plane)



(f) Collection Efficiency vs Wire Number (v-plane)

Figure 27: 2D histograms of the charge collected vs the drift time for each wire plain are shown in (a), (c), and (e). Plots of the calculated wire efficiencies for each wire plane are shown in (b), (d), and (f).

18 wires in the downstream region of the TPC. Tracks that started in the upstream region of the TPC were ignored when analyzing the first neutron data, thus providing a fairly uniform track reconstruction efficiency for all other events.

5. Conclusion

The construction, commissioning and successful deployment of the Mini-CAPTAIN detector in a high-energy neutron beam at LANSCE was the result of an extensive campaign lasting several years. The detector commissioning included several run cycles, each demonstrating that TPC cold electronics, once properly configured, can run stably and efficiently. The electronic modules from both Brookhaven National Lab and Nevis Lab have proven very consistent, with fast and clean signals. Measurement of minimizing ionizing particles produced a nominal signal-to-noise of roughly 9 before any software filter was applied. The successful extraction of signals from the TPC, in coincidence with time-of-flight signals from the PDS, resulted in a first neutron cross-section measurement in argon. The experience gained during the Mini-CAPTAIN campaign will prove useful in the future operation of CAPTAIN.

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