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# Front End Electronics for the Jet Drift Chamber 

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## ABSTRACT

The Jet Drift Chamber employs a charge division readout scheme which requires two amplifiers per sense wire. Since there are 690 sense wires, 1380 amplifiers are needed. It follows that each end cap must accommodate 690 amplifiers which makes packaging and cooling critical problems. As a result, 30 six-layer surface mount boards each containing 24 (23 used) channels of amplifiers have been made. Each amplifier could accept up to 2 pC of charge and could provide a linear differential output of $\pm 300 \mathrm{mV}$ which is used to drive a twisted pair transmission line up to $70^{\prime}$ in length. The power dissipation of each board is 1.5 W .

## INTRODUCTION

A charge division 1,2 readout scheme requires two amplifiers per sense wire; i.e., one amplifier at each end of the wire. There are a total of 30 sectors in the drift chamber and each sector contains 23 wires. It follows that a total of 1380 amplifiers are required with 690 amplifiers housed in each end cap. The space available in each end cap to house these amplifiers is very limited; hence, the packaging and power dissipation of the system must be carefully designed and planned. Figure 1 shows the charge division readout scheme employed by the Jet Drift Chamber. The ratio of charge collected at the two ends of the sense wire having a length $L$ is:

$$
Q a / Q b=\left(L-X_{0}\right) / X_{0}
$$

The front end electronics make use of proven and readily available components which provide 24 channels of amplifier on a single card. The card has a dimension of 11 cm in length and 5 cm in width. Figure 2 is a schematic diagram of the front end showing 4 channels of the 24 amplifiers on a single card.

and

$$
Q=Q a+Q b
$$



Fig. 2 Partial schematic diagram of the JDC front end.

All components used are of surface mount type. The circuit board contains six layers of circuit traces, ground and power planes. Components are mounted on the two outside surfaces. The card was designed to minimize crosstalk between channels by isolating adjacent channels with either ground traces or power bus traces. ${ }^{4}$ Components and circuit traces placement has also been implemented carefully to reduce crosstalk to a minimum. The ground layer has been used to shield critical crosstalk paths wherever possible. The total thickness of the six-layer board is $\sim 2$ man.

## FRONT END CIRCUIT DESCRIPTIONS

The resistor network at the inputs of the amplifiers is used for chamber wire crosstalk cancellation. The values of the resistors depend on the characteristics of the drift chamber. A diode pair is connected from the input signal line to ground to provide some protection for the amplifier from voltage transients. An integrated 0.7 pF capacitor is connected to the input of each amplifier. These capacitors serve as charge injection capacitors for testing the amplifiers.

The Fujisu MB43468 amplifier chips ${ }^{3}$ are used as the preamplifiers. Each chip contains 4 amplifiers. Each amplifier output is attenuated by an adjustable voltage divider to obtain the proper input signal level for the MCl0116 line drivers. The 24 miniature adjustable resistors (square shapes) can be seen in Fig. 3.

Since the output signals of this front end must be transmitted to an Analog To Digital Converter (ADC) some $70^{\prime}$ away, the MC10116 line drivers are used in the linear mode to provide differential outputs to drive twisted pair transmission lines. This substantially reduced noise pickup along this $70^{\prime}$ run. Each MC10116 chip contains 3 differential amplifiers. It follows that, in order to make up 24 amplifier channels, six MB43468 chips and eight MC 10116 chips are required on each board. The 14 chips can be seen in Fig. 4.

In order to reduce the heat load on the boards and therefore in the drift chamber, the pull down resistors for the differential amplifiers are located external of the board at the receiving ends of the twisted pair transmission lines.


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Fig. 3 Photograph of one side of the board showing 24 adjustable resistors.


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Fig. 4 Photograph of one side of the board showing 14 amplifier chips.

## SYSTEM PERFORMANCE

Figure 5 shows the gain of the MB43468 amplifier to be $\sim 900 \mathrm{mV} / \mathrm{DC}$ with the $1 \%$ compression point occuring at an input charge of 2.35 pC. Figure 6 shows two system transfer functions of the MB43468/ MCl0116 amplifier. The $45 \mathrm{mV} / \mathrm{pC}$ gain was used to cover an input range of $0-2.3 \mathrm{pC}$. The $84 \mathrm{mV} / \mathrm{pC}$ gain was used to cover an input range of $0-1.2 \mathrm{pC}$. The primary limitation is caused by the ADC's $\pm 100 \mathrm{mV}$ input signal range used in the data acquisition system. The secondary limitation is the $\pm 300 \mathrm{mV}$ linear range of the MC10176 differential amplifier. Disregarding the $A D C$ limitation, the best gain achieved using the MB43468/MC10116 system would be around 150 $m V / D C$ with an input dynamic range of $0-2$ pC. However, if the dynamic range is small, the gain of the system can be set to a much higher value.


XBL 887-2626

Fig. 5 Transfer function of a typical MB43468 amplifier.

The gain compression of a typical amplifier operating in the $\pm 100 \mathrm{mV}$ output voltage range is $<1 \%$ at the maximun output voltage level. If a $\pm 300 \mathrm{mV}$ output voltage range is used, the pull down resistors must be changed to 270 ohms. The gain compression at the maximum output signal in this case would become $5 \%$.

The systen gain can be optimized by adjusting the attenuator between the MB43468 output and the input of the MC10116 which has a typical voltage gain of 5 . The attenuator also allows for correction of unequal channel gain. Figure 7 shows the outputs of a typical amplifier with a test charge injected at the signal input port. The $10-90 \%$ risetime of a typical amplifier is 12 ns . The equivalent noise at the input is 3000 electrons rms whereas the input impedence across the bandwidth of interest is $\sim 90$ ohms.

The test charge is injected into all the amplifiers at the same time via the 0.7 pF integrat.ed capacitors. It was noticed that the 4 channels closest to the center of the board produced output signals somewhat contaminated by the test voltage step which was producing a contamination charge via some stray capacitance in the range of $20 \%$ of the injected charge. Since this test function is not intended to be used for precise calibration, no further effort was used to improve this situation. Crosstalk between channels is less than 1.5\%.

The total power consumption of each card, including the pull down resistors, amounts to 2.25 W . Locating the 461 K ohms pull down resistors at the receiving ends of the twisted pair transmission line will reduce the power dissipation per board to 1.5 W . Assuming a total of 30 cards per end cap, the power dissipation per end cap will be 45 W or 90 W for both end caps. The system power consumption amounts to 135 W with 45 W being dissipated by the pull down resistors. Further power reduction could be achieved by employing a pulse power scheme. 4 Notice that if 270 ohms are used for pull down resistors, the total power consumption will be higher.


Fig. 6 Transfer functions of a typical channel of MB43468/MC10116 amplifier.


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Fig. 7 Typical differential output waveshapes of an amplifier channel across 56 ohm loads.

## CONCIUSIONS

Although the front end was designed for a specific drift chamber, the flexibility of the system could make it useful in many other applications. The differential outputs are very useful for driving long transmission lines to reduce noise pickup. The low cost ( $\$ 220$ per board) and the compact size could also be attractive for many applications. Further reduc-tion of the test pulse contamination could be made by shielding portions of the test pulse input trace with the ground plane and the power plane, preventing it from injecting stray charge into the nearby amplifier channels.

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