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Author

Graven, R.M.

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A CHARGE ASYMMETRY MEASUREMENT SYSTEM

R. M. Graven, J. H. Brewer, R. J. Budnitz, R. L. McCarthy[†], D. H. Miller^{††}

Lawrence Berkeley Laboratory
 University of California
 Berkeley, California 94702
 December 1, 1971

ABSTRACT

A computer controlled data collection and analysis system was built and used at the Lawrence Berkeley Laboratory's Bevatron to measure the charge asymmetry in the decay $K_L^0 \rightarrow \pi^+ \mu^- \bar{\nu}$. The system records the states of 144 scintillation counters arranged into twelve hodoscopes, and it can test the operation of each counter. Information from pulse height analyzers, a digital voltmeter, time-to-amplitude converters, event clock, and scalers was recorded with each event. Using a PDP-9 computer, over 25 million events were recorded on magnetic tape during the tuning and data phases of the experiment. A CRT, programmed to display histograms and geometrical models of the experimental information as it was accumulated, was a valuable diagnostic tool allowing rapid visual assessment of both broad hodoscope and specific counter anomalies. The computer's power was greatly expanded by the addition of external control registers, hardware priority resolvers, event matrix logic, additional programmable flags, and input-output transfer request lines, in addition to software priority levels and the usual program interrupt line and peripherals. Inexpensive dual discriminators, nanosecond coincidence gates, testing circuits and new packaging methods were all used in developing this system.

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[†] Present address: University of Chicago, Chicago, Illinois, 60637.

^{††} Present address: Northwestern University, Evanston, Illinois, 60202.

INTRODUCTION

An on-line data acquisition and analysis system that controls, checks, and records the states of 144 scintillation counters has been built and used to perform a charge asymmetry experiment. The experiment, performed at the Lawrence Berkeley Laboratory's Bevatron, was a measurement of the relative decay rates of the K_L^0 meson into the states $\pi^+\mu^-\nu$. Figures 1 and 2 show the electronics and computer system. The equipment used to perform the experiment is described by a plan and elevation sketch in Fig. 3. Figure 4 is a photograph showing the magnet, absorber, and a few of the hodoscopes.

K_L^0 decays were detected by the usual coincidence and anti-coincidence techniques, using Chronetics logic modules to determine when a pion and a muon occur in coincidence (± 20 ns). This system allowed the trigger to be the result of either a simple coincidence or a complex ten-fold coincidence of various special counter(s) from selected hodoscope(s). Those counters comprising the trigger could either be selected manually or changed dynamically under program control. Events to be recorded were selected by a matrix of segments of hodoscopes. This allowed detailed on-line studies of the geometrical and range distributions of the particles being detected. The matrix selection criteria could be changed from a very lax to a quite stringent requirement, depending on the nature of the effect being studied. Results of a test (e.g., changing the magnetic field or adding counter(s) to the trigger) could be displayed in graphical form on a CRT.

A block diagram of the electronics and computer system is shown in Fig. 5. A modular construction technique was used to minimize down-time by simplifying repair, change and expansion. Fig. 6 is an example of the logic chain traversed by a typical photomultiplier tube pulse.

Additional hardware was used to expand the number and length of I/O busses that were connected to the PDP-9. Priority resolver logic distinguished between four data channels. Each data channel used a core location to store word-counter and current-address information. In addition, each of four Automatic Priority Interrupt (API) levels could trap to any of eight core locations, providing 32 trap addresses. The priorities between flags connected to the same level were resolved in the module called "omnibus" (see Fig. 5). Voltage level converters between PDP-9 and IC levels were also provided.

The utility bin is similar to a NIM bin, except for special wiring to accept non-standard size-2 NIM modules. Each utility bin module provides the interface electronics for a different function, which simplifies not only functional expansion of the electronics, but also debugging of the combined hardware/software system. The modules used were: Flags and Input-Output Transfer (FIOT), External Control Registers (ECR), Automatic Priority Interrupt (API), Nidbus to NIne (NINI), and Accumulator Read-in Module (ARM). This left one spare module slot.

Four flags and 12 input-output transfer signals were used to synchronize the computer program to the pulsed beam of the Bevatron; to change the time sequences in the test generator; and to control the timing of event transfers from the logic to the computer. External control registers provided one bit for each counter, used to pass or block the output pulse after it had been discriminated and stretched (see Fig. 6). This individual gate on each counter could be changed manually or under computer control, allowing dynamic cross-talk checks. The automatic-priority-interrupt module allowed the experimenter to choose among

four API levels or a program-interrupt line for each of the following: keyboard, printer, clock, and spare. The accumulator read-in module provided a way to enter external information into the accumulator.

The NINI in Fig. 5 is the interface module which controlled the transfer of 18-bit words from the logic and its peripherals into core. A nuclear data bus-ing system called NIDBUS sequentially selected the instruments to be read-out and transferred the information. It could operate manually (one word at a time); or slowly (typing out on an IBM Selectric typewriter); or rapidly (at micro-second speeds). Also, it could be operated independently of the computer, so that several system-debugging operations could be done simultaneously.

The matrix and control block decided which events were to be accepted for a preliminary analysis on the PDP-9. It also provided the timing and reset pulses for the logic.

LOGIC

Figure 6 illustrates the logic chain traversed by a typical photomultiplier tube output pulse. First, small signals from noise and background particles were discriminated against. Secondly, the surviving (and therefore "interesting") pulses are stretched to a uniform $20 \text{ ns} \pm 1 \text{ ns}$. Thirdly, the state of a pre-set ECR bit determined if the information for this counter was to proceed. If so, two pulse output signals were available to feed logic arrays. Since the signals are NIM standard (zero to -0.7 volts), they could be used as inputs to Chronetics series 150 logic units. The fourth element is a 140 ns delay line.

Pulse output signals fed a Chronetics trigger-logic array, taking advantage of the versatility of these modules. One result of the trigger logic was a prompt-strobe signal whose width determined the resolving time of the next coincidence gate. Another result was a delayed-strobe signal which was timed to occur one Bevatron revolution (403 ns) after the prompt-strobe. This signal was used to measure the random counting rate of each counter. Two bits of memory stored this "signal" and "random" information for later transmission to the computer. An asynchronous or programmable test pulse could also be introduced to test the entire logic chain.

DISPLAY

Figure 7 illustrates how the information describing each event was displayed on the CRT. For example, a muon going up and bending to the left (west) penetrates the absorber, and a pion going down and bending to the right (east) which stops in the lead wall, are indicated in Fig. 3 and graphically shown in Fig. 7. The display pattern, which represents inactivated scintillators by dotted lines and activated counters by intensified solid lines, is generated from a binary record of the event. Two patterns were displayed for each event to graphically represent the state of each counter during both the prompt-strobe (40 ns) and delayed-strobe (40 ns) sampling intervals. The letters A,P,S,R,T, etc., in Fig. 3 are labels for the various hodoscopes. The alphanumeric R1 and D1 in Fig. 7 label the Real and Delayed patterns for the nth (first) event. The CRT was also programmed to display histograms of (1) any counter bank, (2) all counters, (3) matrix combinations of banks, (4) pulse height spectrums, and (5) test routines.

For precise comparisons both octal and decimal dumps of an accumulation of events were provided. Programmable alarms alerting the experimenters when tolerances were exceeded or when the hardware failed were crucial to the success of the experiment.

DISCRIMINATORS

Table I is a comparison between commercial Chronetics discriminators and the dual discriminators developed for this data collection system. The cost savings are a result of using MECL II* integrated circuits as well as eliminating the NIM-type mounting hardware, BNC connectors, switches and the versatility that NIM packaging provides. We used several bins of Chronetics Model 150's logic units in our trigger decision logic, and the 1968 model LRL discriminators in our data collection logic. The combination provides the following advantages: lower cost, programmable testing, compact packages, and smaller power supplies, without sacrificing versatility where it is needed.

* Motorola trademark

TABLE I
DUAL DISCRIMINATORS

	CHRONETICS		UC - LRL	
Model	101	151	1968	1970
Weight	900 grams	1,600 grams	58 grams	60 grams
Power	3.1 watts	14 watts	0.5 watts	1.5 watts
Volume	4900 cm ³	4300 cm ³	66 cm ³	66 cm ³
Minimum Threshold	100 mv	50 mv	50 mv	30 mv
Transit Time	7 ns	15 ns	18 ns	18 ns
Bandwidth	100 Mc	200 Mc updating	25 Mc	200 Mc updating
Outputs into 50 Ω	2 ea. - 300 Mv 1 ea. - 2.5 v	4 ea. - 700 Mv 1 ea. - 1 volt	2 ea. - 700 Mv 1 ea. complement	2 ea. - 700 Mv 1 ea. complement
Test Circuit	Yes	No	Yes Programmable	Yes programmable
Gated Output	yes	yes	yes	yes
Threshold Adjustment	knob discrete	knob continuous	screwdriver continuous	screwdriver continuous
Output Width Adjustment	knob 4.5ns $\rightarrow$ 1 μ s	knob and screwdriver 3ns $\rightarrow$ 5 μ s	capacitor and screwdriver 20 ns $\rightarrow$ 5 μ s	capacitor and screwdriver 3ns $\rightarrow$ 5 μ s
APPROXIMATE COST	\$775	\$880	\$50	\$75

RESULTS

Both electrical noise from spark chambers in other nearby experiments and the Bevatron's oscillating magnetic field made small signal measurements difficult. Separate cables and line drivers from the omnibus to each peripheral helped to isolate problems and simplify repair. A background radiation level of about five neutrons/cm²/sec in the 1 - 3 MeV energy range did not seem to affect the equipment. The environment was also polluted by dust, vibrations, several earthquakes, and voltage surges when the high voltage power lines feeding the laboratory were sabotaged. Despite these obstacles, electronic problems rarely hindered the progress of the experiment during two years of operation.

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FIGURE CAPTIONS

1. Data collection electronics
2. Expanded computer system and peripherals
3. Experimental equipment sketch
4. Magnet, absorber, and hodoscopes
5. Data collection, analysis, and recording system block diagram
6. Typical logic chain
7. CRT display of one event

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CBB 702-1094

Fig. 1

-11-

CBB 702-1098

Fig. 2

XBL 7011-4161 A

Fig. 3

CBB 7012-5660

Fig. 4

XBL712-2813

Fig. 5

XBL7III-4733

Fig. 6

XBB 721-19

Fig. 7

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