

Lawrence Berkeley National Laboratory

Lawrence Berkeley National Laboratory

Title

HRTEM image simulations for gate oxide metrology

Permalink

<https://escholarship.org/uc/item/6bz3s2tw>

Authors

Taylor, Seth
Mardinly, John
O'Keefe, Michael A.
et al.

Publication Date

2000-02-10

HRTEM Image Simulations for Gate Oxide Metrology

S. Taylor^{1,3}, J. Mardinly², M.A. O'Keefe³, and R. Gronsky^{1,3}

¹Department of Materials Science and Engineering, University of California, Berkeley, CA 94720

²Intel Corporation, Materials Technology Department, Santa Clara, CA 95052

³National Center for Electron Microscopy, Lawrence Berkeley National Laboratory, Berkeley, CA 94720

DISCLAIMER

This document was prepared as an account of work sponsored by the United States Government. While this document is believed to contain correct information, neither the United States Government nor any agency thereof, nor The Regents of the University of California, nor any of their employees, makes any warranty, express or implied, or assumes any legal responsibility for the accuracy, completeness, or usefulness of any information, apparatus, product, or process disclosed, or represents that its use would not infringe privately owned rights. Reference herein to any specific commercial product, process, or service by its trade name, trademark, manufacturer, or otherwise, does not necessarily constitute or imply its endorsement, recommendation, or favoring by the United States Government or any agency thereof, or The Regents of the University of California. The views and opinions of authors expressed herein do not necessarily state or reflect those of the United States Government or any agency thereof, or The Regents of the University of California.

Ernest Orlando Lawrence Berkeley National Laboratory is an equal opportunity employer.

Ernest Orlando Lawrence Berkeley National Laboratory – LBNL-47220

HRTEM Image Simulations for Gate Oxide Metrology

S. Taylor^{1,3}, J. Mardinly², M.A. O'Keefe³, and R. Gronsky^{1,3}

¹Department of Materials Science and Engineering, University of California, Berkeley, CA 94720

²Intel Corporation, Materials Technology Department, Santa Clara, CA 95052

³National Center for Electron Microscopy, Lawrence Berkeley National Laboratory, Berkeley, CA 94720

High resolution transmission electron microscopy (HRTEM) has found extensive use in the semiconductor industry for performing device metrology and characterization. However, shrinking device dimensions (gate oxides are rapidly approaching 10Å) present challenges to the use of HRTEM for many applications, including gate oxide metrology. In this study, we performed HRTEM image simulations of a MOSFET device to examine the accuracy of HRTEM in measuring gate oxide thickness. Length measurements extracted from simulated images were compared to actual dimensions in the model structure to assess TEM accuracy. The effects of specimen tilt, specimen thickness, objective lens defocus and coefficient of spherical aberration (C_s) on measurement accuracy were explored for nominal 10Å and 16Å gate oxide thicknesses.

The gate oxide was modeled as an amorphous silicon oxide situated between a gate electrode and substrate, both modeled as single crystal Si(100). Image simulations of the sandwich structure were performed in cross-section (with Si[110] parallel to beam direction) using the multislice approximation for a 200 kV microscope with $C_s=0.5\text{mm}$.¹ Amorphous slices were added to the top and bottom of the specimen to simulate the amorphization that occurs during typical specimen preparation. The actual gate oxide thickness, T , is defined as the distance between the bounding Si atoms in the model structure (Fig. 1a). The gate oxide thickness was also measured directly in pixels from the simulated image (Fig. 1b). We use a statistical routine to calculate the standard deviation in pixel intensity for each horizontal row (or y-coordinate) in the simulated image (Fig. 1c). Local minima in the standard deviation, which correspond to low-intensity regions between Si[110] dumbbells, were used to calibrate the image length scale. The measured gate oxide thickness was then compared to the actual (model) thickness to assess accuracy for a variety of microscope and specimen conditions (Table I).

Results reveal no consistent trends in measurement accuracy as a function of specimen thickness, specimen tilt, or objective lens defocus. Specimen tilts as large as 25 mrad do not necessarily decrease measurement accuracy, but in some cases may actually improve it.² Surprisingly, minimum contrast defocus ($f = -156\text{Å}$) is observed to yield greater accuracy than Scherzer defocus ($f = -425\text{Å}$) for at least one specimen thickness (246Å). Although Scherzer defocus yields the most accurate representation of the projection of the specimen potential for a thin specimen, the best defocus for accurate projection of thicker structures moves steadily in the overfocus direction as specimen thickness is increased.³ Measurement errors (percent) for the 16Å gate oxides are generally lower than those for the 10Å gate oxides. Images simulated using $C_s = 0.0\text{ mm}$ consistently yield the most accurate measurement of gate oxide thickness, with an error of 1% or less. Significantly, measurement accuracy in these simulations is maintained even at larger specimen thicknesses that are likely to be encountered in practice. Based on these results, we believe that accurate metrology of 10Å gate oxides (i.e. to within several per cent) is feasible on a consistent basis using a C_s -corrected microscope. However, the effects of Si-SiO₂ interface roughness on measurement accuracy should be explored in greater detail to assess the applicability of zero- C_s microscopy to metrology of real systems.

REFERENCES

1. J.M. Cowley and A.F. Moodie, Acta Cryst. 10 (1957) 609.
2. M.A. O'Keefe and V. Radmilovic, Proc Ann MSA Meeting 51 (1993) 980.
3. M.A. O'Keefe et al., Mat. Res. Soc. Symp. Proc. 159 (1989) 453.
4. Work funded by Intel Corporation. Access to computing facilities at the National Center for Electron Microscopy is gratefully acknowledged. The NCEM is supported by the Director, Office of Science, Office of Basic Energy Sciences, Material Sciences Division, of the U.S. Department of Energy, under contract No. DE-AC03-76SF00098.

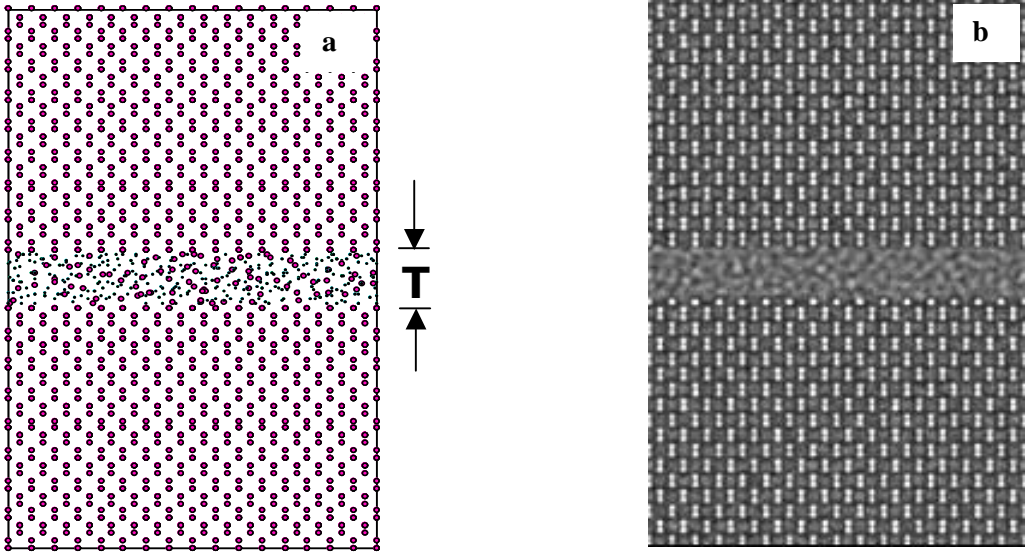


Fig 1

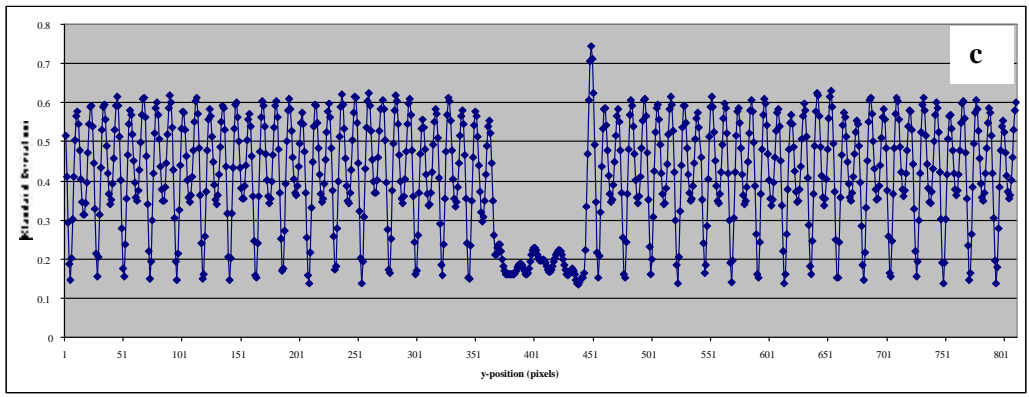


FIG 1 (a) Model structure, (b) simulated image and (c) plot of standard deviation versus y-coordinate for 10Å gate oxide structure at $C_s=0.0$ mm (specimen thickness = 154Å).

Table 1

Sp e i m e n Th i c k n e s s (Å)	Sp e i m e n T i l t (m r a d)	D e f o c u s (Å)	C_s (mm)	M o d e l Th i c k n e s s (Å)	M e a s u r e d Th i c k n e s s (Å)	% Error
154	0	-425	0.5	10.56	9.84	-6 . 8
154	0	-156	0.5	10.56	11.40	+ 80
154	0	-20	0.0	10.56	10.44	-1 . 1
154	12.6	-425	0.5	10.56	9.12	-1 3 . 6
154	25	-425	0.5	10.56	10.68	+ 11
246	0	-425	0.5	10.56	8.88	-1 5 . 9
246	0	-156	0.5	10.56	10.92	+ 34
246	0	-20	0.0	10.56	10.56	0.0
246	6.3	-425	0.5	10.56	9.48	-1 0 . 2
154	0	-425	0.5	16.29	18.94	+ 05
246	0	-425	0.5	16.29	19.21	+ 09
246	0	-156	0.5	16.29	19.55	+ 27